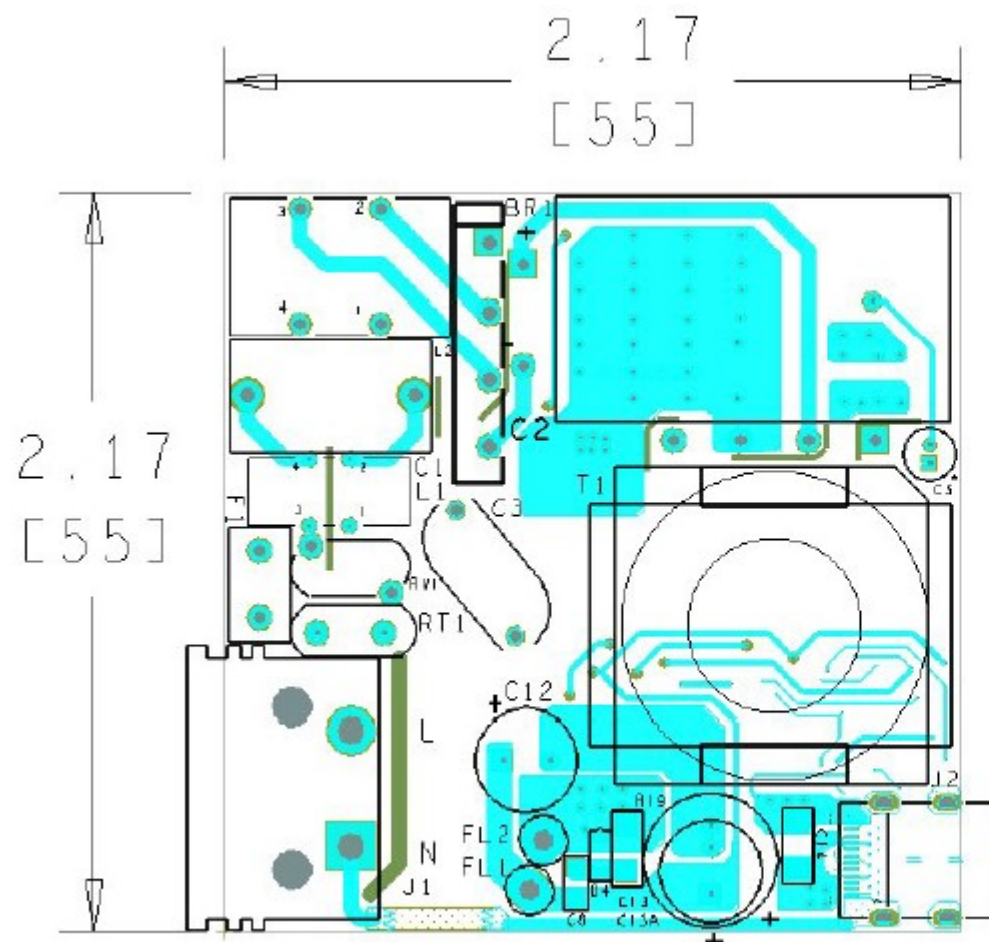




Printed Circuit Layout, Top.

