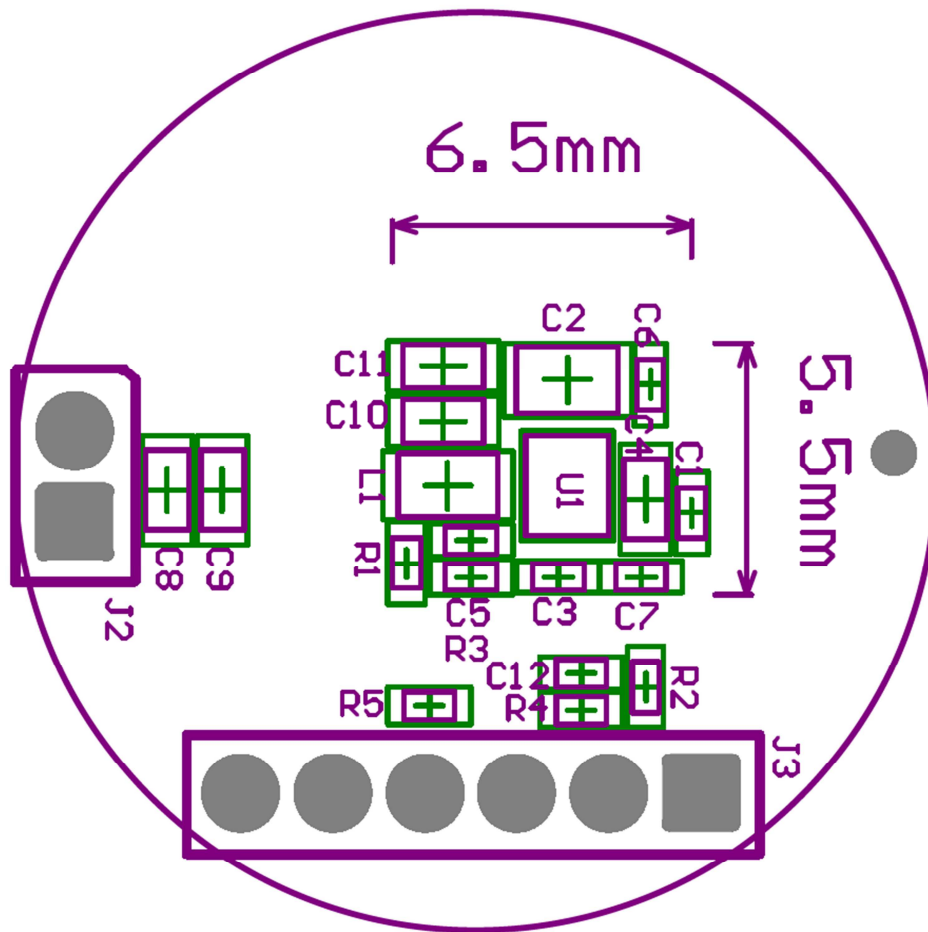
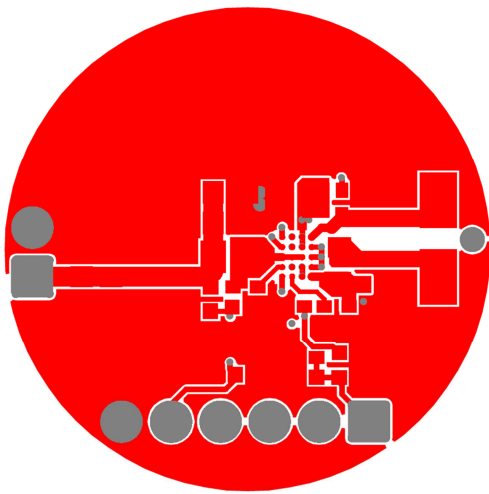

D. Board Layout

The diagram below shows the locations of the components used in the TSWIRX-5V2-EVM PCB.

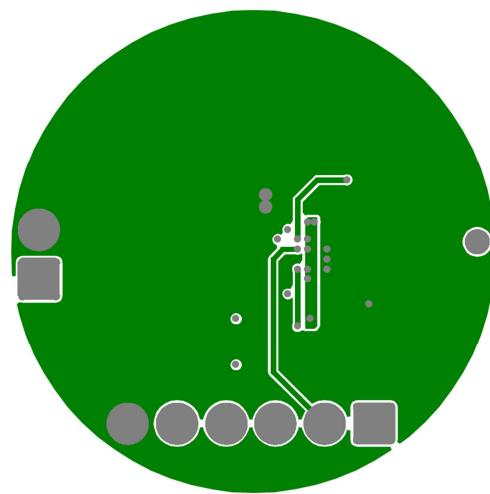


E. Board Layers

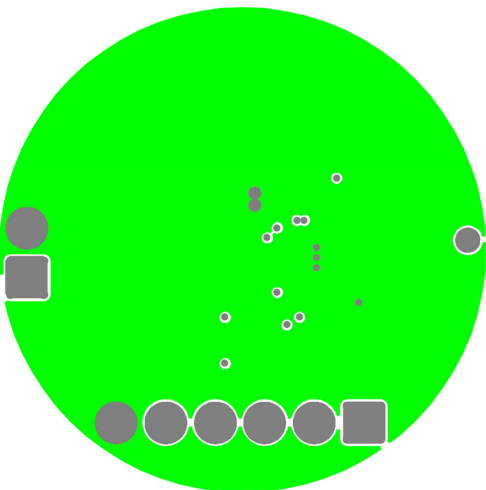
The TSWIRX-5V2-EVM PCB is based on a four layer design as shown below. The ground plane in layer two is recommended to reduce noise and signal crosstalk. The EVM placed all components on the top of the board for easier evaluation of the system. End product versions of this design can be made significantly smaller by distributing components on both sides of the board. The Gerber files for this artwork can be downloaded from the Semtech web page.



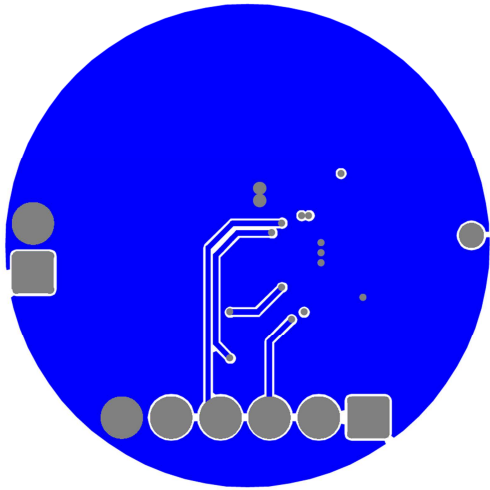
Top Layer



Ground Plane



Signal Layer



Bottom Layer