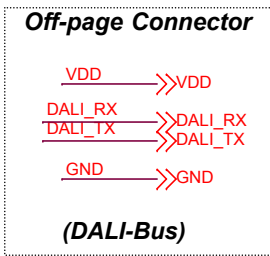


Title			
Power			
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Off-page Connector

VDD >> VDD
DALI_RX >> DALI_RX
DALI_TX >> DALI_TX
GND >> GND

(DALI-Bus)



Off-page Connector

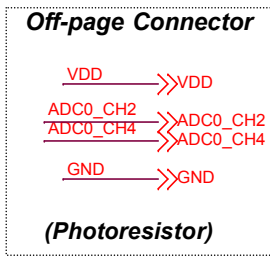
VDD >> VDD
DALI_RX >> DALI_RX
DALI_TX >> DALI_TX
GND >> GND

(DALI-Bus)

Off-page Connector

The diagram shows four horizontal lines on the left, each representing a signal or power connection. From top to bottom, they are labeled: VDD, ADC0_CH2, ADC0_CH4, and GND. Each line is connected to a double-lined arrow pointing to the right. The arrows are labeled on the right side: VDD, ADC0_CH2, ADC0_CH4, and GND. The connections for ADC0_CH2 and ADC0_CH4 are shown as a single arrow pointing to a single label, indicating they share a common connection point.

(Photoresistor)



Off-page Connector

The diagram shows four horizontal lines on the left, each representing a signal or power connection. From top to bottom, they are labeled: VDD, ADC0_CH2, ADC0_CH4, and GND. Each line is connected to a double-lined arrow pointing to the right. The arrows are labeled on the right side: VDD, ADC0_CH2, ADC0_CH4, and GND. The connections for ADC0_CH2 and ADC0_CH4 are shown as a single arrow pointing to a single label, indicating they share a common connection point.

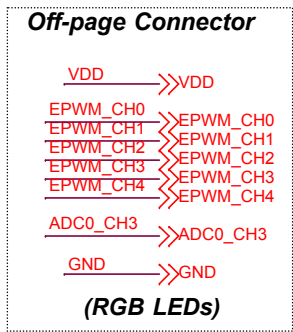
(Photoresistor)

Off-page Connector

The diagram illustrates an off-page connector with the following connections:

- VDD** is connected to **VDD**.
- EPWM_CH0** is connected to **EPWM_CH0**.
- EPWM_CH1** is connected to **EPWM_CH1**.
- EPWM_CH2** is connected to **EPWM_CH2**.
- EPWM_CH3** is connected to **EPWM_CH3**.
- EPWM_CH4** is connected to **EPWM_CH4**.
- ADC0_CH3** is connected to **ADC0_CH3**.
- GND** is connected to **GND**.

(RGB LEDs)



Off-page Connector

VDD

EPWM_CH0

EPWM_CH1

EPWM_CH2

EPWM_CH3

EPWM_CH4

ADC0_CH3

GND

(RGB LEDs)

Diagram illustrating the pin connections for the headers:

- JP8:** DALI_TX (pin 1), Tiny_TX (pin 2). Header: HEADER 2.
- JP9:** DALI_RX (pin 1), Tiny_RX (pin 2). Header: HEADER 2.
- JP10:** ECAP_P0 (pin 1), ECAP_P1 (pin 2). Header: HEADER 2.
- JP11:** ADC0_CH2 (pin 1), ADC0_CH1 (pin 2), ADC0_CH4 (pin 3), ADC0_CH3 (pin 4). Header: HEADER 4.

(Option & External Header)

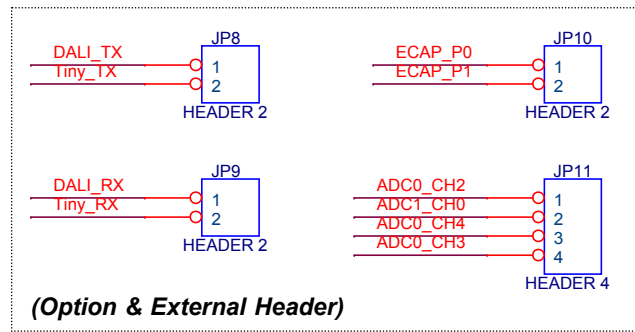


Diagram illustrating the pin connections for the headers:

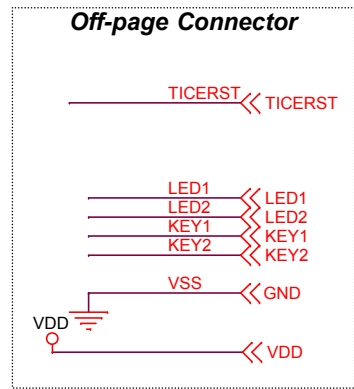
- JP8:** DALI_TX (pin 1), Tiny_TX (pin 2). Header: HEADER 2.
- JP9:** DALI_RX (pin 1), Tiny_RX (pin 2). Header: HEADER 2.
- JP10:** ECAP_P0 (pin 1), ECAP_P1 (pin 2). Header: HEADER 2.
- JP11:** ADC0_CH2 (pin 1), ADC0_CH1 (pin 2), ADC0_CH4 (pin 3), ADC0_CH3 (pin 4). Header: HEADER 4.

(Option & External Header)

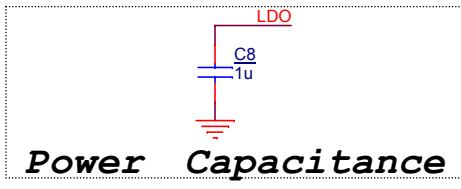
Off-page Connector

The diagram illustrates an off-page connector with the following connections:

- TICERST** (Signal) connected to **TICERST**
- LED1** (Signal) connected to **LED1**
- LED2** (Signal) connected to **LED2**
- KEY1** (Signal) connected to **KEY1**
- KEY2** (Signal) connected to **KEY2**
- VSS** (Power) connected to **GND**
- VDD** (Power) connected to **VDD**



A circuit diagram showing a capacitor labeled 'C8' with a value of '1u' connected between the output of an 'LDO' (Low Dropout Regulator) and ground. The text 'Power Capacitance' is written below the diagram.

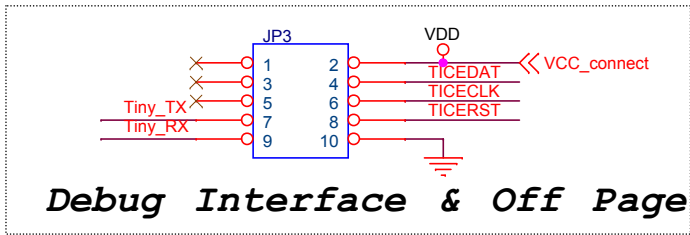


TP1 NC PB3

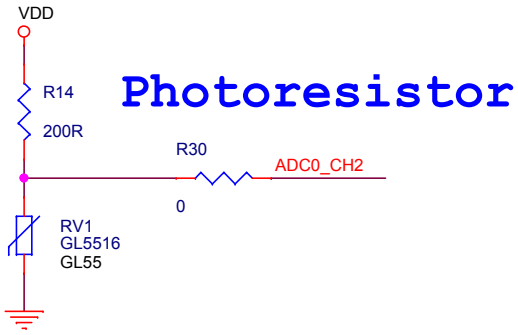
Test Pin



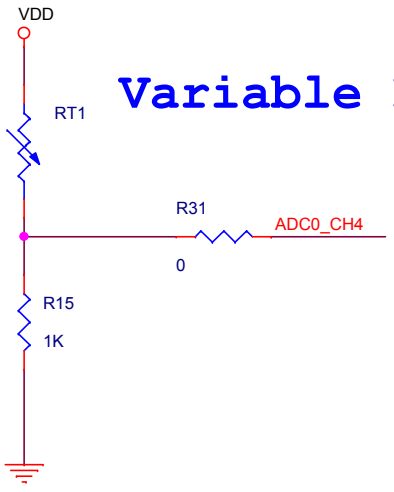
Debug Interface & Off Page



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Photoresistor



Variable Resistor

Off-page Connector

VDD

>>>VDD

ADC0_CH2

>>>ADC0_CH2

ADC0_CH4

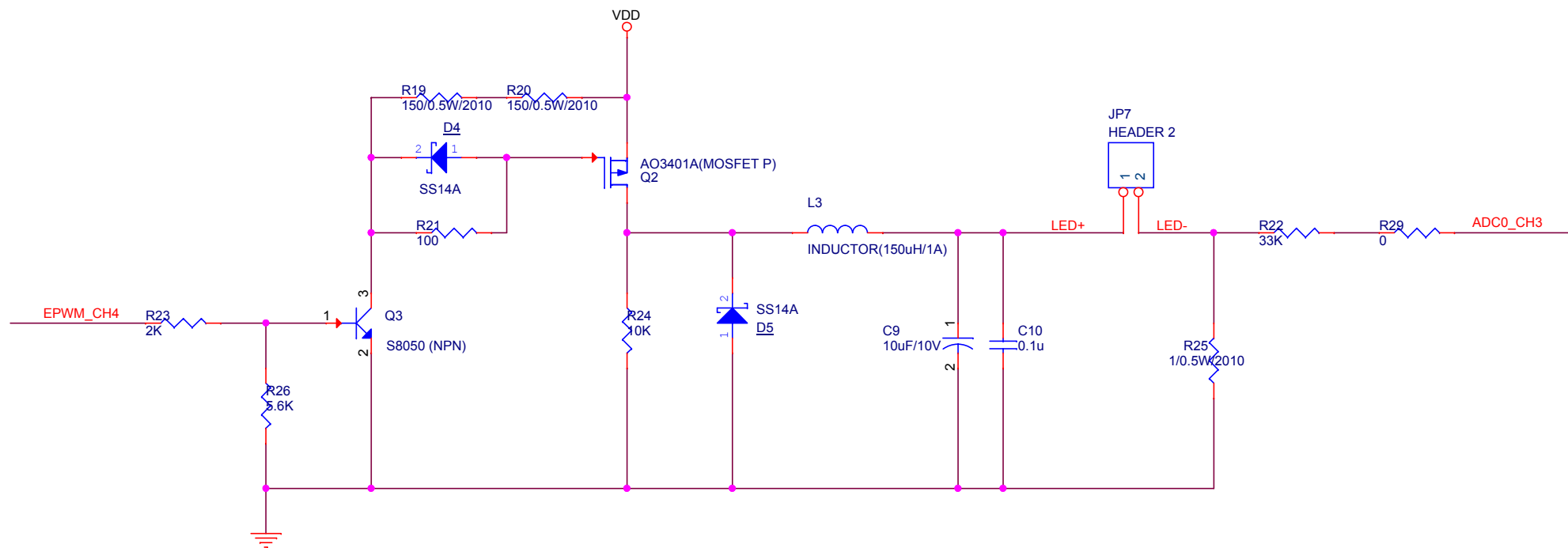
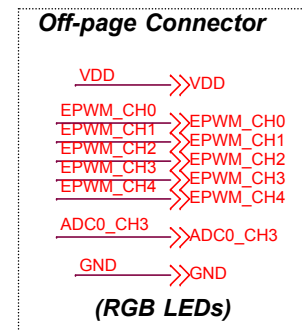
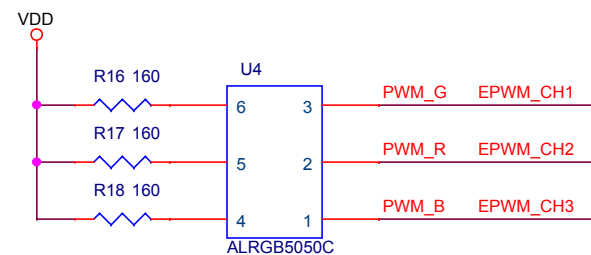
>>>ADC0_CH4

GND

>>>GND

(Photoresistor)

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Title				
LEDs				
Size A4	Document Number 2			Rev 1.0
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