

# RT7739GGE System Performance Test Report

Product P/N	RT7739GGE
Project Code + Version	TKV151B03
Date Code	
EVB Number	RT7739-45W-V0
Document Version	

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Date: 2015/10/02

Application Engineering Department  
System Development Division

## CONTENTS

Revision History.....	4
1. Summary.....	5
2. Test Facility .....	7
3. Function Block Diagram .....	7
4. Test Circuit .....	8
5. Electrical Characteristics Test Items.....	9
5.1 Input Characteristics .....	9
5.1.1 Efficiency .....	9
5.1.2 Efficiency at 10% load of full rated output current.....	11
5.1.3 Input power under no load & Light load .....	11
5.1.4 Standby power test .....	12
5.1.5 Start up time .....	13
5.1.6 Brown-In and Brown-Out.....	14
5.1.7 Slope compensation and sub-harmonic .....	15
5.1.8 Hold up time .....	16
5.2 Output characteristics .....	17
5.2.1 Line regulation and load regulation.....	17
5.2.2 Output ripple & noise .....	18
5.2.3 Overshoot and rising time at start up.....	19
5.2.4 Undershoot and falling time at power off.....	21
5.2.5 Load transient and cycle test .....	22
5.2.6 Peak Load transient and cycle test.....	23
5.3 Protections .....	24
5.3.1 OCP and recovery.....	24
5.3.2 SC and recovery .....	25
5.3.3 Photo-coupler short (Vout OVP) .....	26
5.3.4 Photo-coupler short (VDD OVP) .....	27
5.3.5 Output diode short at 90Vac and 265Vac .....	28
5.3.6 Rcs open at 90Vac and 265Vac.....	30
5.3.7 Over Temperature Protection.....	32
5.4 Key Component Waveforms .....	33
5.4.1 Voltage stress on MOSFET .....	33

5.4.2 Voltage stress on Secondary Rectifier Diode .....	35
5.5 Standard and safety.....	37
5.5.1 EMI (Test result is peak & Output ground connector to safety ground).....	37
6. Bill of Material .....	39
7. Transformer Specification .....	43

**Revision History**

Version	Description	Date
00	Initial	2015/10/02

## 1. Summary

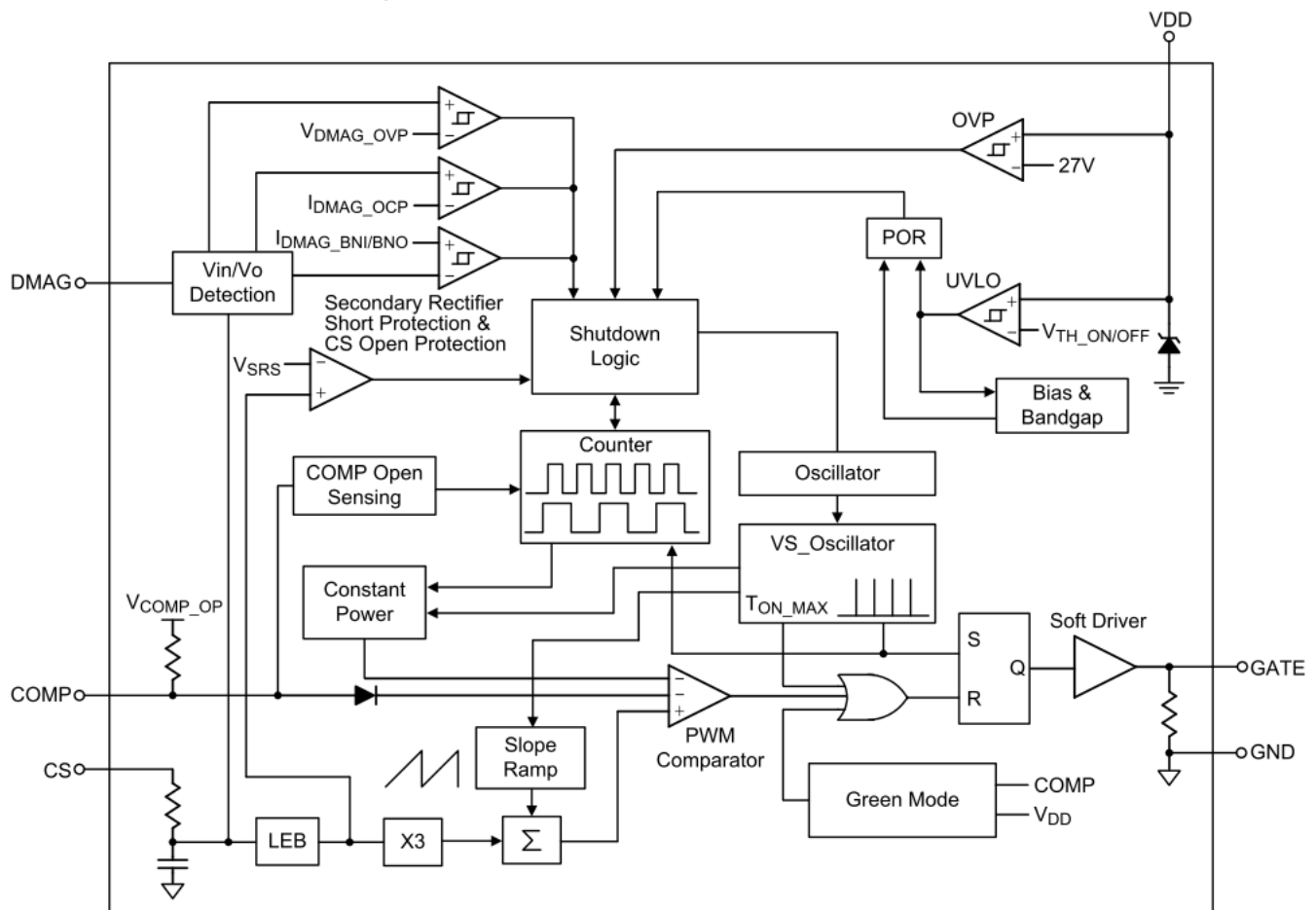
Item No.	Test Item	Test Result	Comment
<b>5.1</b>	<b>Input Characteristics</b>		
5.1.1	Efficiency	Pass	
5.1.2	Efficiency at 10% load of full rated output current	Pass	
5.1.3	Input power under no load	Pass	
5.1.4	Standby power test	Pass	
5.1.5	Start up time	Pass	
5.1.6	Brown-In and Brown-Out	Pass	
5.1.7	Slope compensation and sub-harmonic	Pass	
5.1.8	Hold up time	Pass	
<b>5.2</b>	<b>Output Characteristics</b>		
5.2.1	Line regulation and load regulation	Pass	
5.2.2	Output ripple & noise	Pass	
5.2.3	Overshoot and rising time at start up	Pass	
5.2.4	Undershoot and falling time at power off	Pass	
5.2.5	Load transient and cycle test	Pass	

5.2.5	Peak Load transient and cycle test	Pass	
5.3	Protections		
5.3.1	OCP and recovery	Pass	
5.3.2	SC and recovery	Pass	
5.3.3	Photo-coupler short(Vout OVP)	Pass	
5.3.4	Photo-coupler short(VDD OVP)	Pass	
5.3.5	Output diode short at 90Vac and 265Vac	Pass	
5.3.6	Rcs open at 90Vac and 265Vac	Pass	
5.3.7	Over Temperature Protection	Pass	
5.4	Key Component Waveforms		
5.4.1	Voltage stress on MOSFET	Pass	
5.4.2	Voltage stress on Secondary Rectifier Diode	Pass	
5.5	Standard and safety		
5.5.1	EMI	Pass	

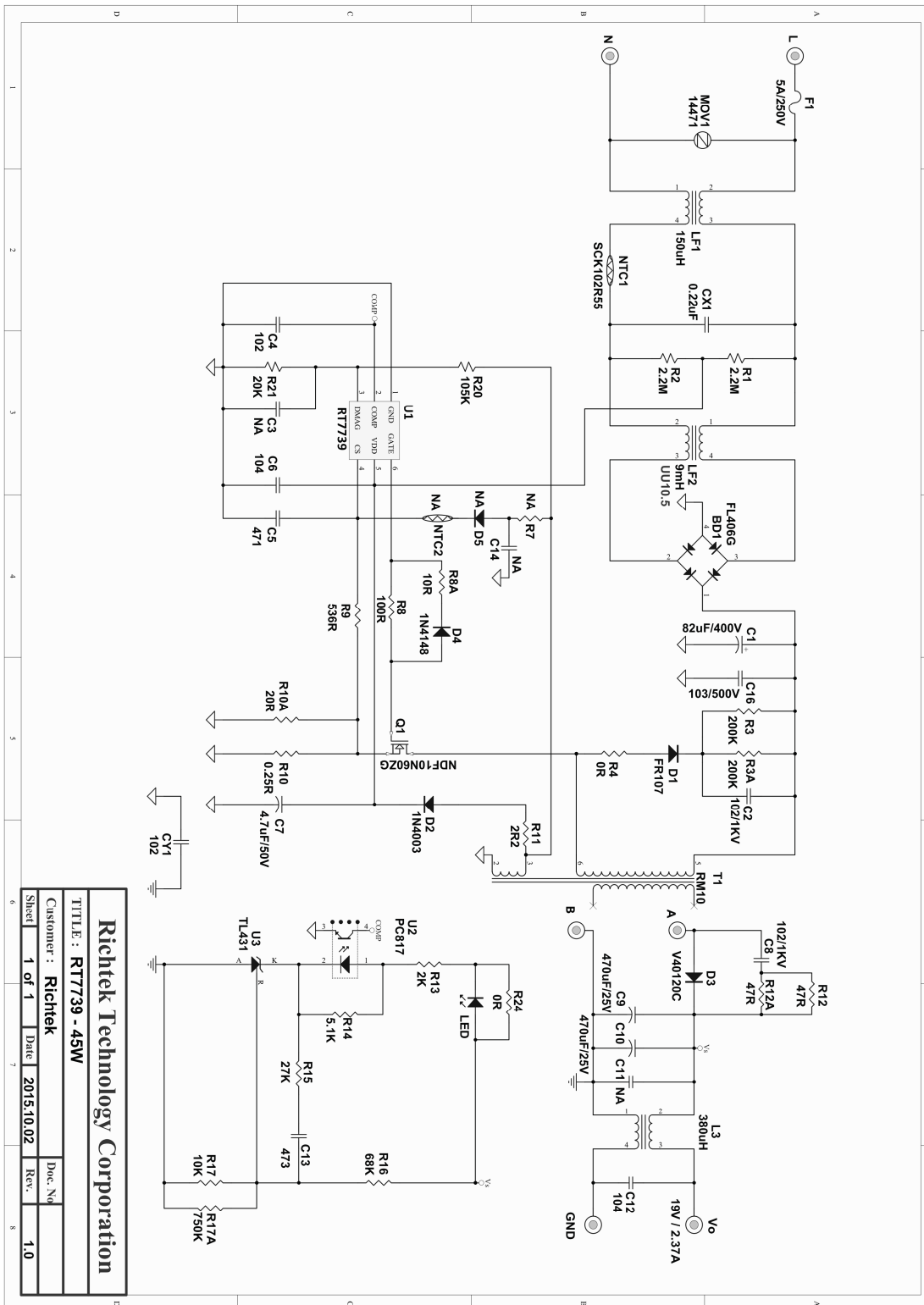
## 2. Test Facility

Instrument	Manufacturer	Type/Serial No.
AC Source	EXTECH	6800
Power meter	YOKOGAWA	WT210
Oscilloscope	LeCroy	44MXi-A
Electronic Load	Chroma	63030
Multi-meter	Fluke	287
Differential probe	LeCroy	ADP305
Current amplifier	LeCroy	CP030

## 3. Function Block Diagram



4. Test Circuit



<b>Richtek Technology Corporation</b>			
TITLE : RT7739 - 45W			
Customer : Richtek		Doc. No.	
Sheet 1 of 1	Date 2015.10.02	Rev. 1.0	



5. Electrical Characteristics Test Items

5.1 Input Characteristics

5.1.1 Efficiency

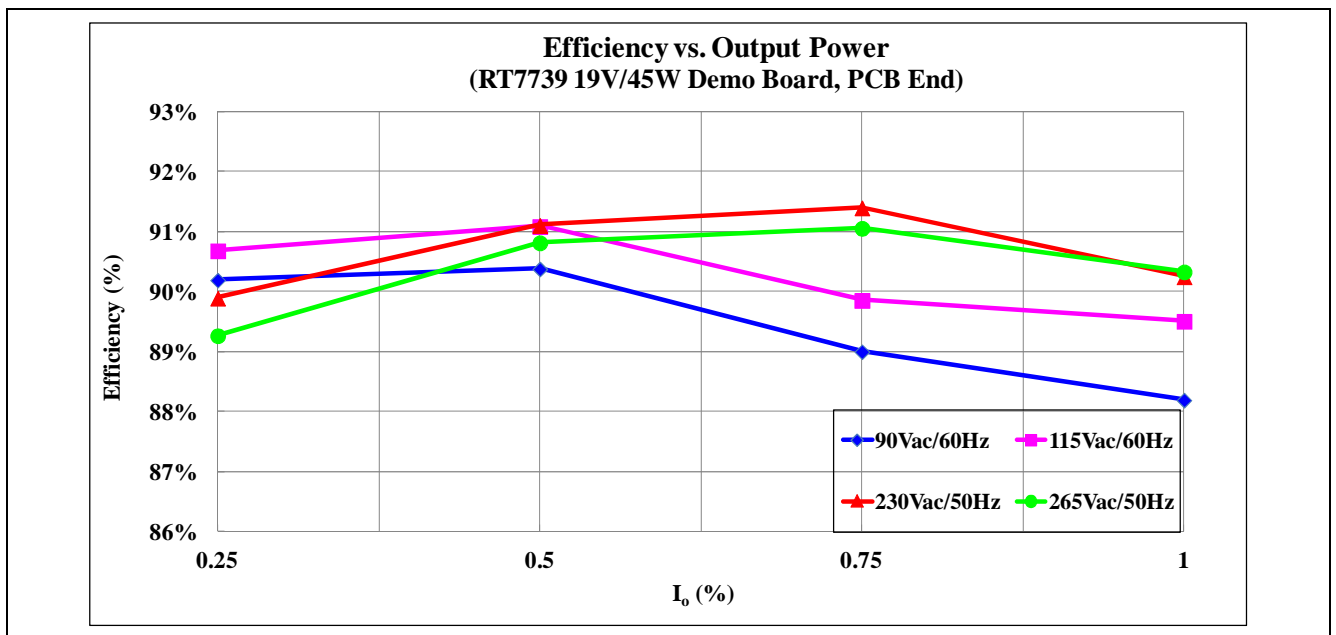
IC Version	TKV151B03
Test method	Efficiency at different load conditions (PS: PCB End)

Test Result

Input Voltage	Measured Efficiency $\eta$ (%) / $f_s$ (kHz)				Average $\eta$ (%)	DOE 6 > 87.73%	CoC Tier2 > 88.85%
	25%	50%	75%	100%			
90Vac / 60Hz	90.20	90.38	89.01	88.20	89.45	Pass	Pass
	22.27	45.01	64.24	64.45			
115Vac / 60Hz	90.69	91.09	89.86	89.51	90.29	Pass	Pass
	22.28	40.83	61.50	64.54			
230Vac / 50Hz	89.90	91.11	91.40	90.25	90.67	Pass	Pass
	22.63	32.26	46.50	60.84			
265Vac / 50Hz	89.27	90.82	91.06	90.34	90.37	Pass	Pass
	22.64	33.07	47.20	61.01			

Note: DOE VI regulation limit :  $\eta \geq 0.071 \cdot \ln(P_{out}) - 0.0014 \cdot P_{out} + 0.67$  (45W spec. >87.73%)

CoC Tier2 regulation limit :  $\eta \geq 0.071 \cdot \ln(P_{out}) - 0.00115 \cdot P_{out} + 0.67$  (45W spec. >88.85%)



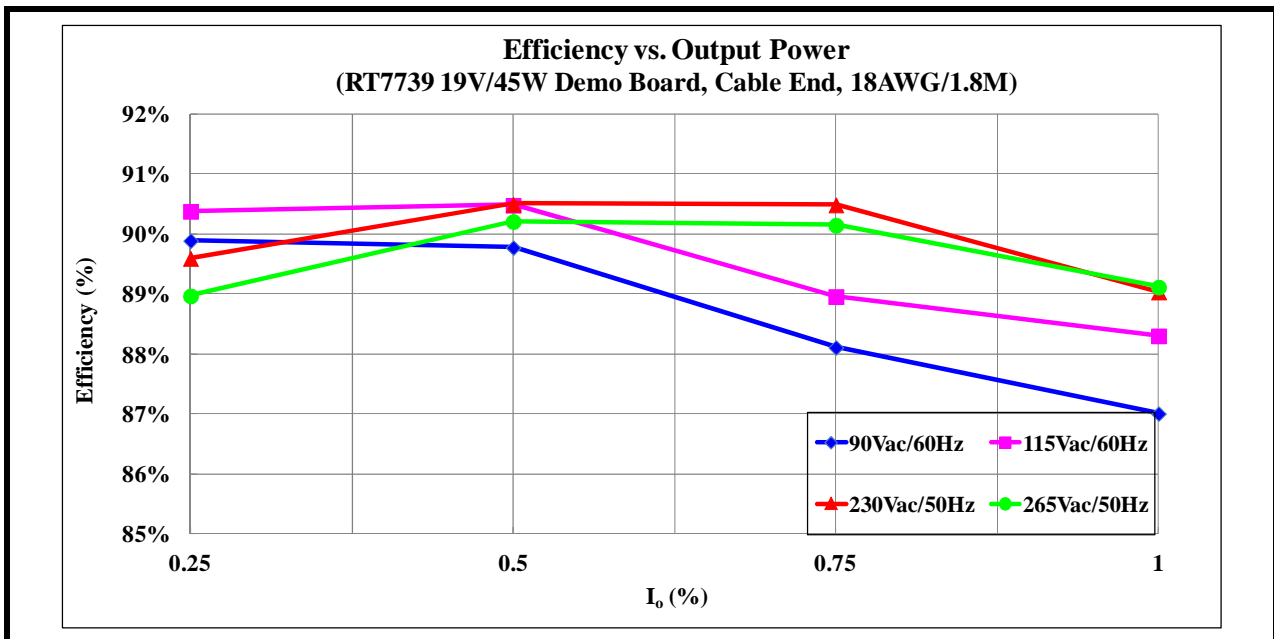
IC Version	TKV151B03
Test method	Efficiency at different load conditions (PS: with 1.8M / 18AWG Cable)

**Test Result**

Input Voltage	Measured Efficiency $\eta$ (%) / $f_s$ (kHz)				Average $\eta$ (%)	DOE 6 > 87.73%	CoC Tier2 > 88.85%
	25%	50%	75%	100%			
90Vac / 60Hz	89.90 22.27	89.78 45.01	88.12 64.24	87.01 64.45	88.70	Pass	Fail
115Vac / 60Hz	90.39 22.28	90.49 40.83	88.96 61.50	88.31 64.54	89.54	Pass	Pass
230Vac / 50Hz	89.60 22.63	90.51 32.26	90.49 46.50	89.04 60.84	89.91	Pass	Pass
265Vac / 50Hz	88.98 22.64	90.22 33.07	90.15 47.20	89.12 61.01	89.62	Pass	Pass

Note: DOE VI regulation limit :  $\eta \geq 0.071 \cdot \ln(P_{out}) - 0.0014 \cdot P_{out} + 0.67$  (45W spec. >87.73%)

CoC Tier2 regulation limit :  $\eta \geq 0.071 \cdot \ln(P_{out}) - 0.00115 \cdot P_{out} + 0.67$  (45W spec. >88.85%)



**5.1.2 Efficiency at 10% load of full rated output current**

IC Version	TKV151B03
Test method	With 1.8M / 18AWG Cable

**Test Result**

Input Voltage	Iout (A)	Measured Eff. (%) / fs (kHz)	Specification	Test result
90Vac / 60Hz	0.237 (0.237)	88.31 / 22.28	> 77.73%	Pass
115Vac / 60Hz		88.18 / 22.27		Pass
230Vac / 50Hz		84.92 / 22.64		Pass
265Vac / 50Hz		83.39 / 22.64		Pass

Note 1: CoC regulation limit :  $\eta \geq 0.071 \cdot \ln(P_{out}) - 0.0014 \cdot P_{out} + 0.57$  (45W spec. > 77.73%)

**5.1.3 Input power under no load & Light load**

IC Version	TKV151B03
Test method	No Load

**Test Result**

Input Voltage	Input wattage (mW)	Specification	Test result
90Vac / 60Hz	28.78	< 75mW	Pass
115Vac / 60Hz	32.29		Pass
230Vac / 50Hz	57.66		Pass
265Vac / 50Hz	71.80		Pass

EPA 4.0 Regulation Limit

IC Version	TKV151B03
Test method	With 1.8M / 18AWG Cable

Output Wattage (W)	Input Wattage (W)		Specification	Test Result
	115V	230V		
0.25W (0.260)	<b>0.330</b>	<b>0.386</b>	< 0.5W ( $\eta > 50\%$ )	<b>Pass</b>
0.5W (0.524)	<b>0.633</b>	<b>0.701</b>	< 0.714W ( $\eta > 70\%$ )	<b>Pass</b>
1W (1.023)	<b>1.202</b>	<b>1.302</b>	< 1.428W ( $\eta > 70\%$ )	<b>Pass</b>
1.15W (1.169)	<b>1.367</b>	<b>1.469</b>	< 1.642W ( $\eta > 70\%$ )	<b>Pass</b>
1.5W (1.521)	<b>1.763</b>	<b>1.877</b>	< 2.142W ( $\eta > 70\%$ )	<b>Pass</b>
1.7W (1.727)	<b>1.991</b>	<b>2.115</b>	< 2.428W ( $\eta > 70\%$ )	<b>Pass</b>
2W (2.013)	<b>2.319</b>	<b>2.451</b>	< 2.857W ( $\eta > 70\%$ )	<b>Pass</b>

#### 5.1.4 Standby power test

IC Version	TKV151B03
Test method	With 1.8M / 18AWG Cable

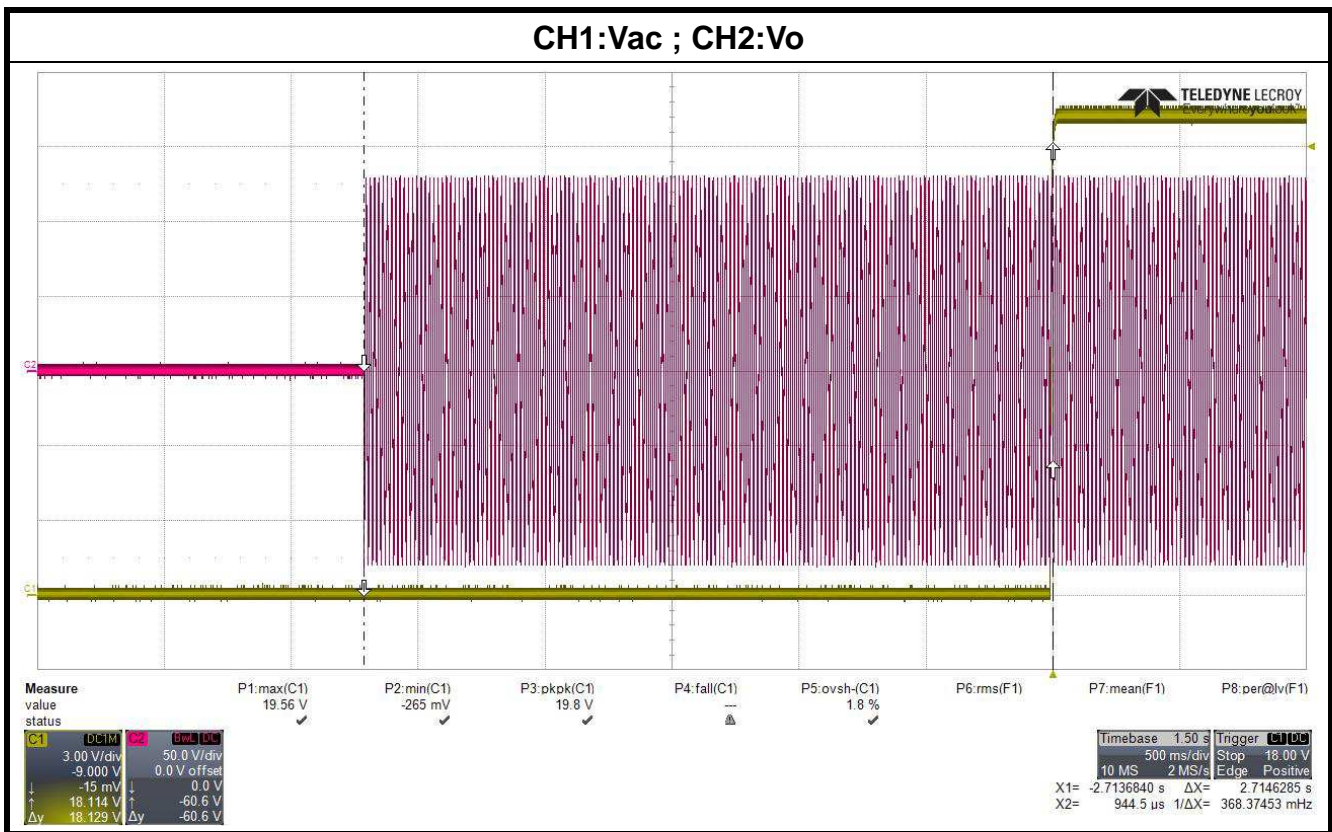
Input Voltage	Input wattage (W)	Pout	Specification	Test result
90Vac / 60Hz	<b>0.626</b>	0.5W (0.524W)	< 1W	<b>Pass</b>
115Vac / 60Hz	<b>0.633</b>			<b>Pass</b>
230Vac / 50Hz	<b>0.701</b>			<b>Pass</b>
265Vac / 50Hz	<b>0.739</b>			<b>Pass</b>

**5.1.5 Start up time**

IC Version	TKV151B03
Test method	With 1.8M / 18AWG Cable

**Test Result**

Input Voltage	Start up time (sec.)	Specification	Test result
90Vac / 60Hz	2.7146	< 3 sec.	Pass



**Note: Output power at full load.**

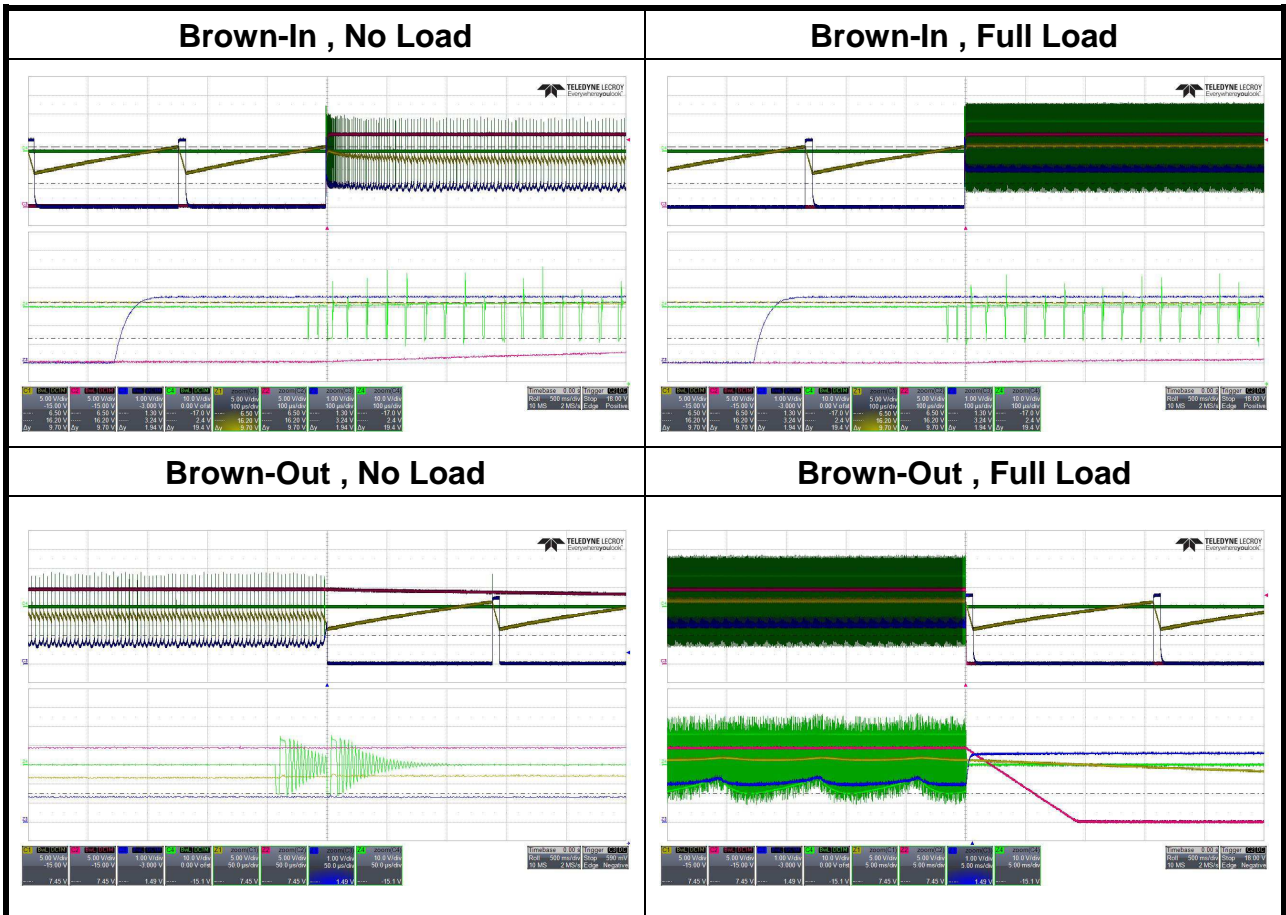
5.1.6 Brown-In and Brown-Out

IC Version	TKV151B03
Test method	

Input Voltage	Input wattage (W)
90Vac / 60Hz	52.51
85Vac / 60Hz	52.79
80Vac / 60Hz	53.17
75Vac / 60Hz	53.88
70Vac / 60Hz	X

BNI/BNO : 78.2Vac / 70.6Vac (No Load)

BNI/BNO : 78.2Vac / 72.1Vac (Full Load)



Note: CH1:V<sub>DD</sub>, CH2:V<sub>O</sub>, CH3:V<sub>COMP</sub>, CH4:V<sub>AUX</sub> waveforms must be taken simultaneously.

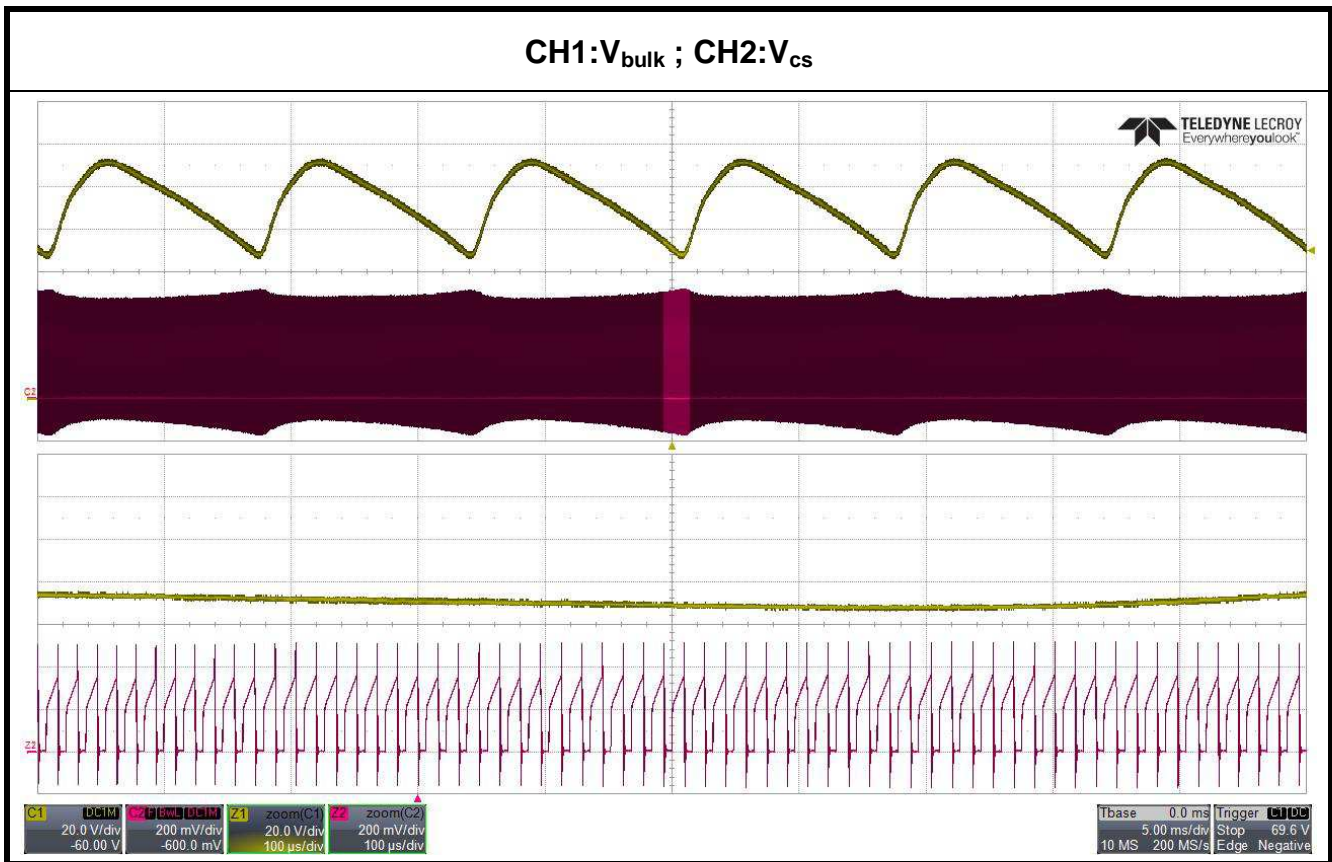


**5.1.7 Slope compensation and sub-harmonic**

IC Version	TKV151B03
Test method	

**Test Result**

Input Voltage	Output load (A)	Test result
80Vac / 60Hz	Full Load	Pass



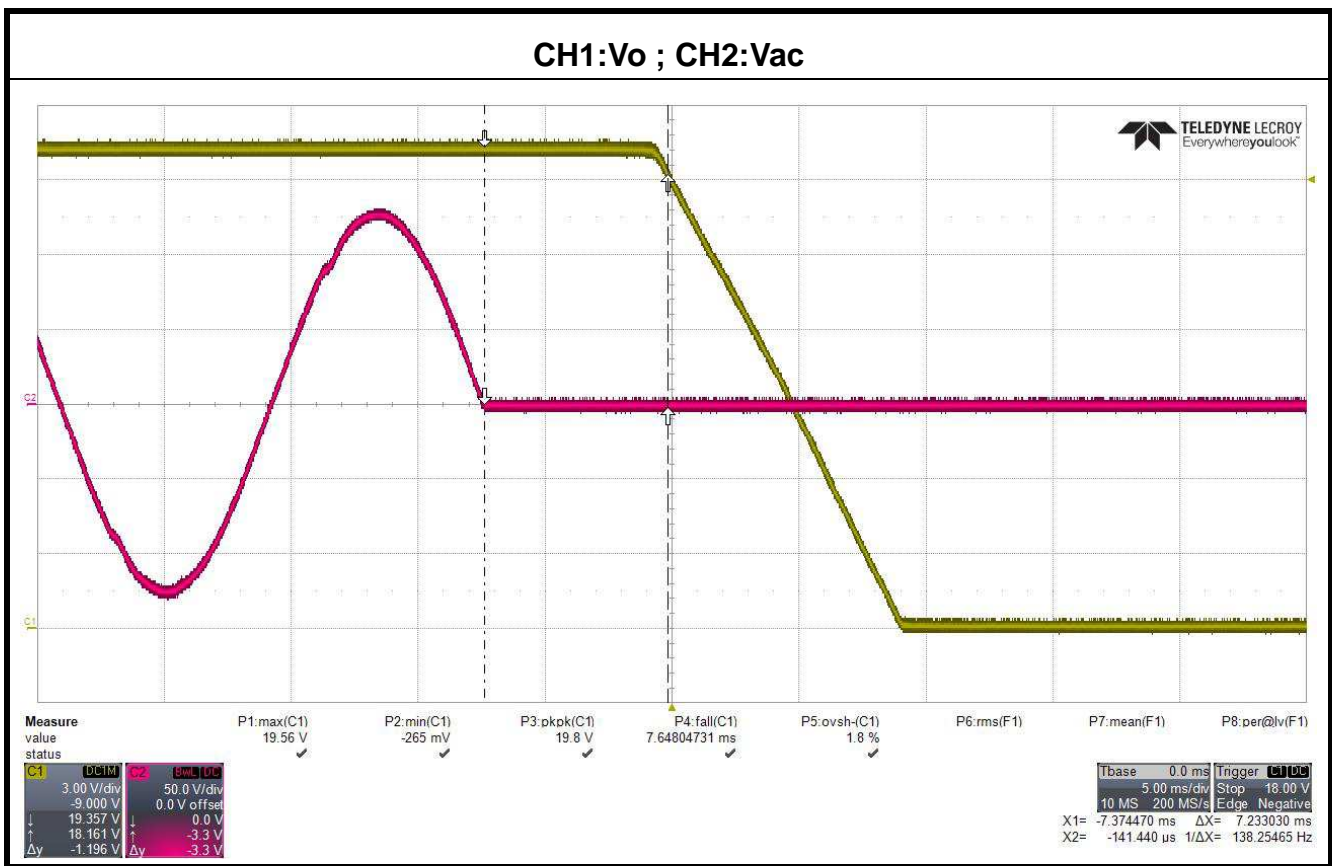
**5.1.8 Hold up time**

IC Version	TKV151B03
Test method	With 1.8M / 18AWG Cable

**Test Result**

Input Voltage	Hold up time (ms)	Specification	Test result
90Vac / 60Hz	7.233	> 5ms	Pass

Note: Output power at full load. (90Vin @ 0 deg)





## 5.2 Output characteristics

### 5.2.1 Line regulation and load regulation

IC Version	TKV151B03
Test method	With 1.8M / 18AWG Cable

#### Test Result

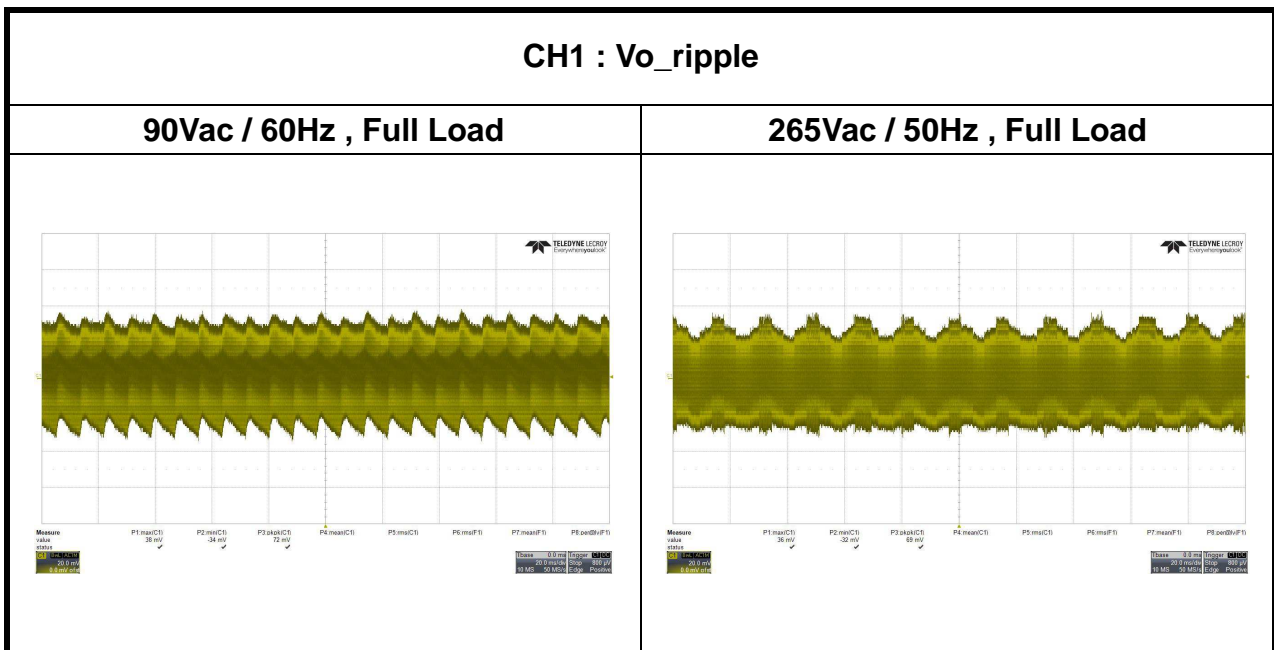
Input Voltage	Measured ( V )			Load Regulation Specification	Result
	Output Full Load	Output Mid Load	Output No Load		
90Vac / 60Hz	19.253	19.422	19.585	Vout ± 5 %	Pass
115Vac / 60Hz	19.253	19.422	19.585		Pass
132Vac / 60Hz	19.253	19.422	19.585		Pass
180Vac / 50Hz	19.253	19.421	19.585		Pass
230Vac / 50Hz	19.253	19.420	19.585		Pass
265Vac / 50Hz	19.253	19.420	19.585		Pass
Line Regulation Specification	Vout ± 1 %				
Result	Pass	Pass	Pass		

**5.2.2 Output ripple & noise**

IC Version	TKV151B03
Test method	With 1.8M / 18AWG Cable

**Test Result**

Input Voltage	Output Full load (A)	Measured (mVp-p)	Specification	Test result
90Vac / 60Hz	2.37	72	< 190mV	Pass
265Vac / 50Hz	2.37	69		Pass



Note: Measured by using a 12 inch twisted pair terminal with a 10uF aluminum electrolytic capacitor and a 0.1uF ceramic in parallel, measured at full load.

**5.2.3 Overshoot and rising time at start up**

IC Version	TKV151B03
Test method	With 1.8M / 18AWG Cable

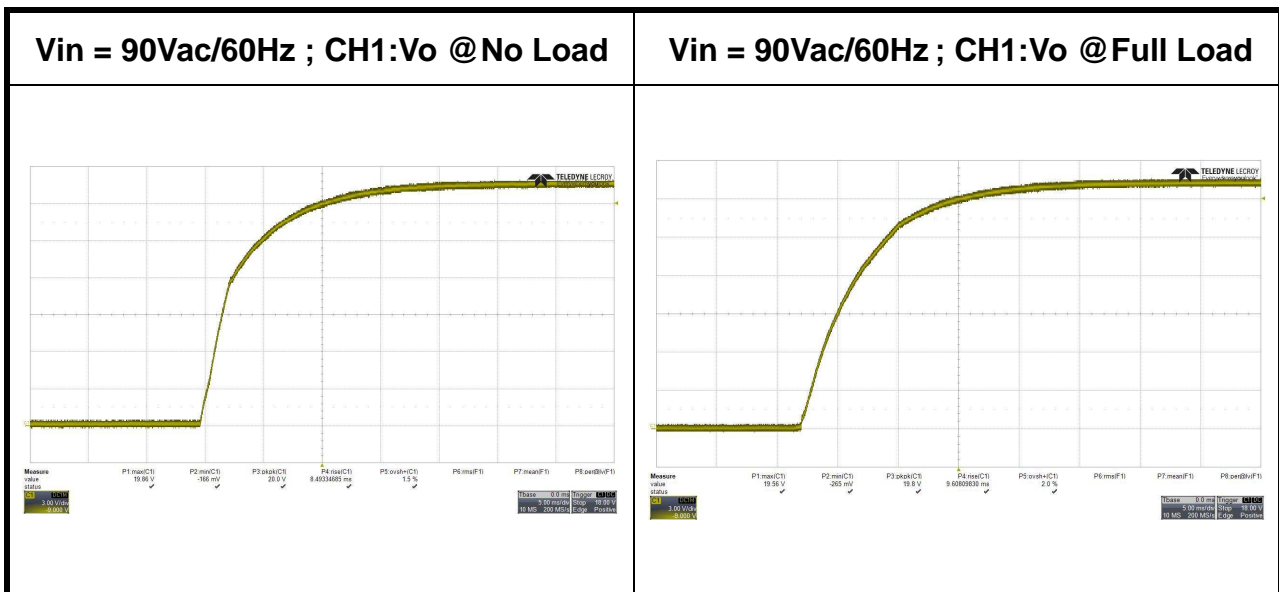
**Test Result**

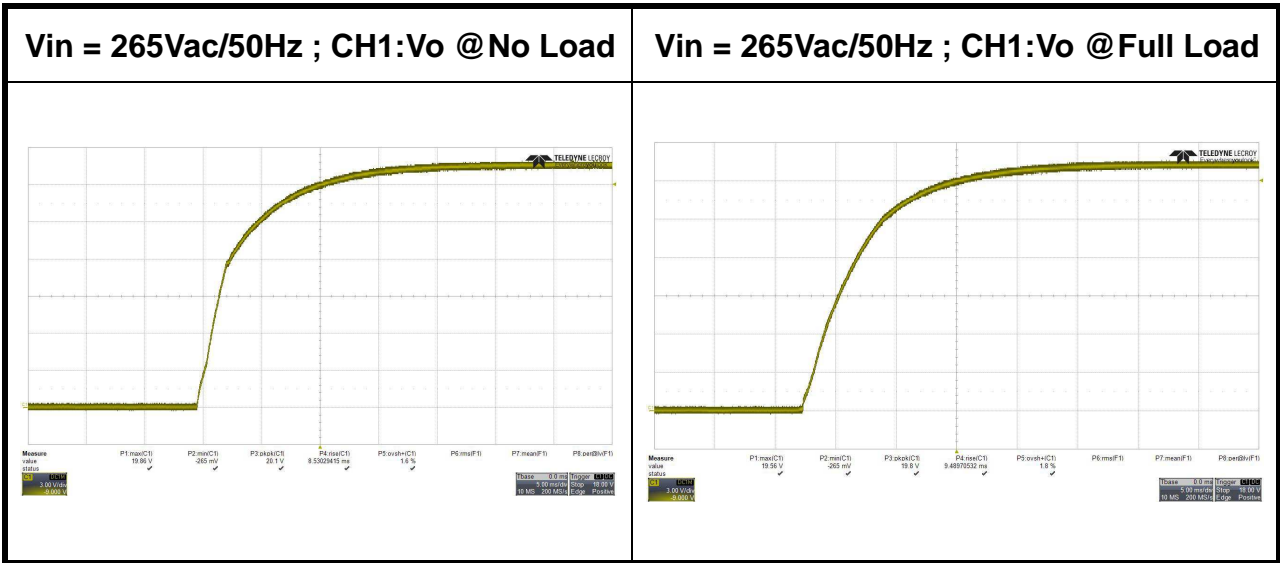
**Output rise time test result**

Input Voltage	Measured ( ms )		Specification (10% ~ 90% Vout)	Test result
	No Load	Full Load		
90Vac / 60Hz	8.493	9.608	< 50ms	Pass
265Vac / 50Hz	8.530	9.489	< 50ms	Pass

**Output overshoot test result**

Input Voltage	Measured ( % )		Specification	Test result
	No Load	Full Load		
90Vac / 60Hz	1.5	2.0	≤ 8% of Vo	Pass
265Vac / 50Hz	1.6	1.8	≤ 8% of Vo	Pass



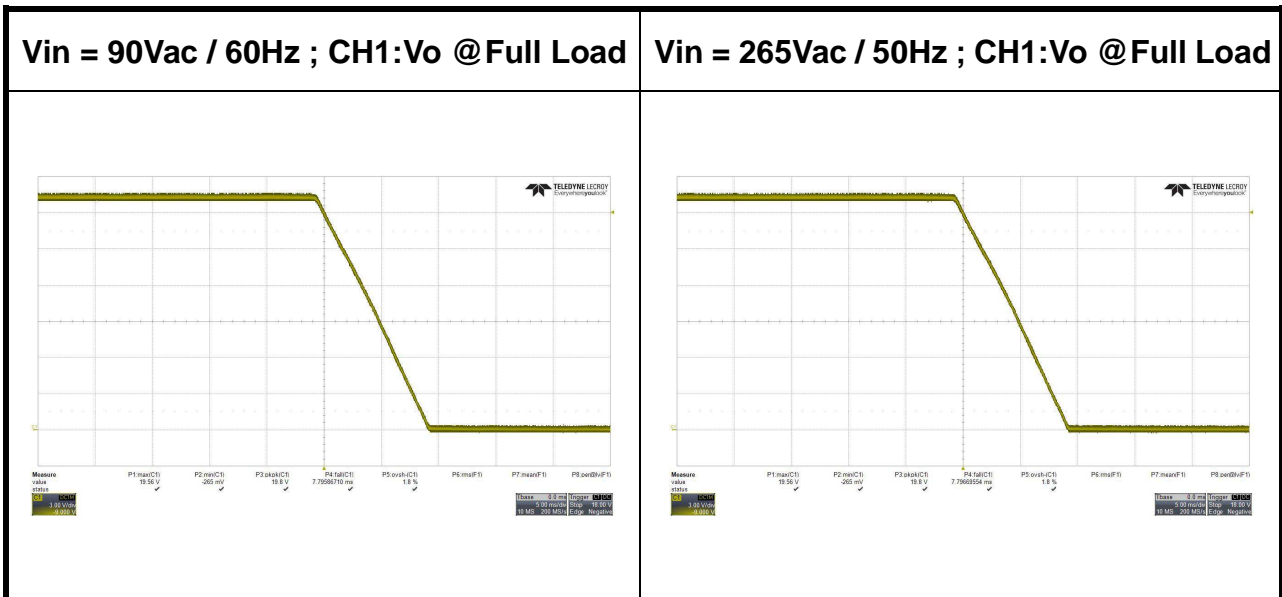


**5.2.4 Undershoot and falling time at power off**

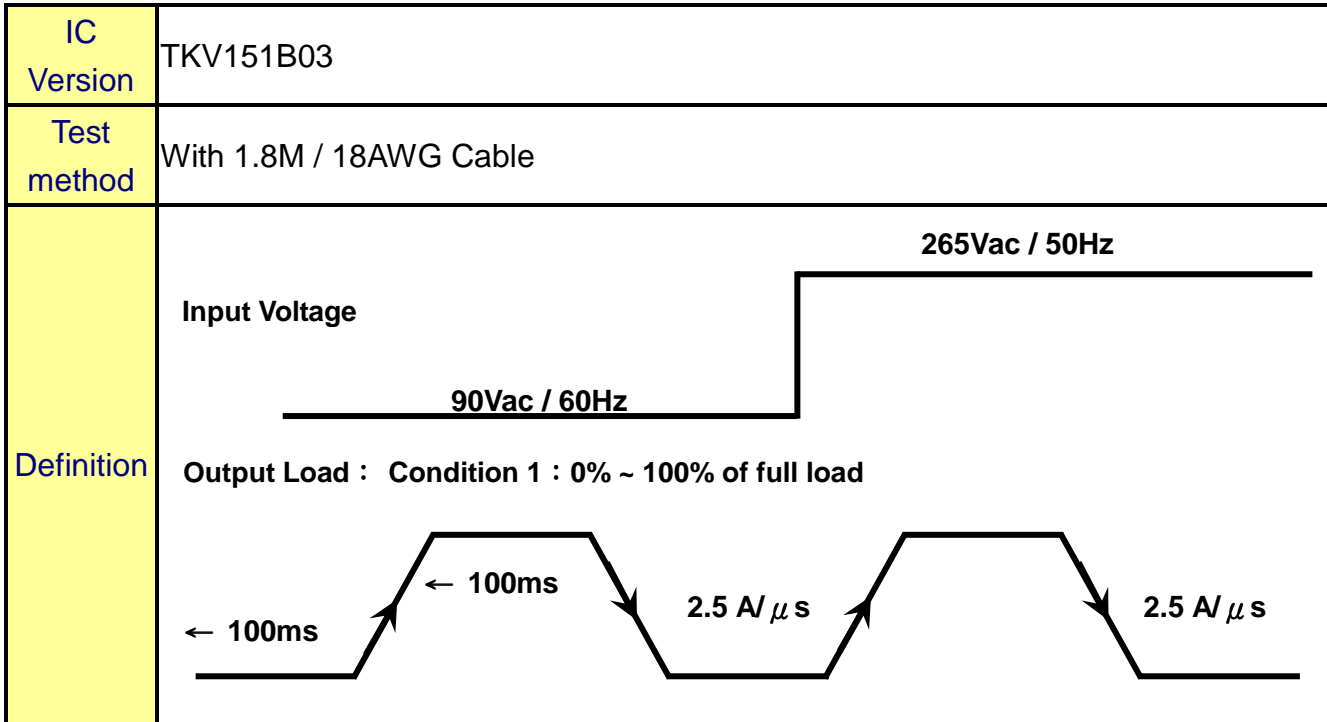
IC Version	TKV151B03
Test method	With 1.8M / 18AWG Cable

**Output falling time test result**

Input Voltage	Measured ( ms )	Specification (90% ~ 10% Vout)	Test result
	Full Load		
90Vac / 60Hz	7.796	< 50ms	Pass
265Vac / 50Hz	7.797	< 50ms	Pass

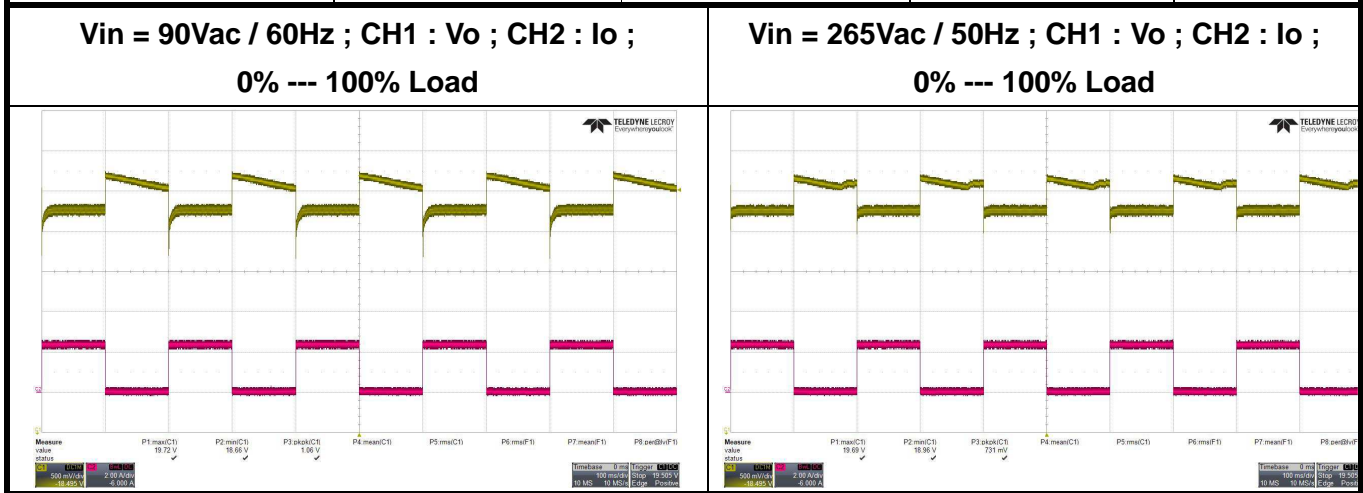


**5.2.5 Load transient and cycle test**

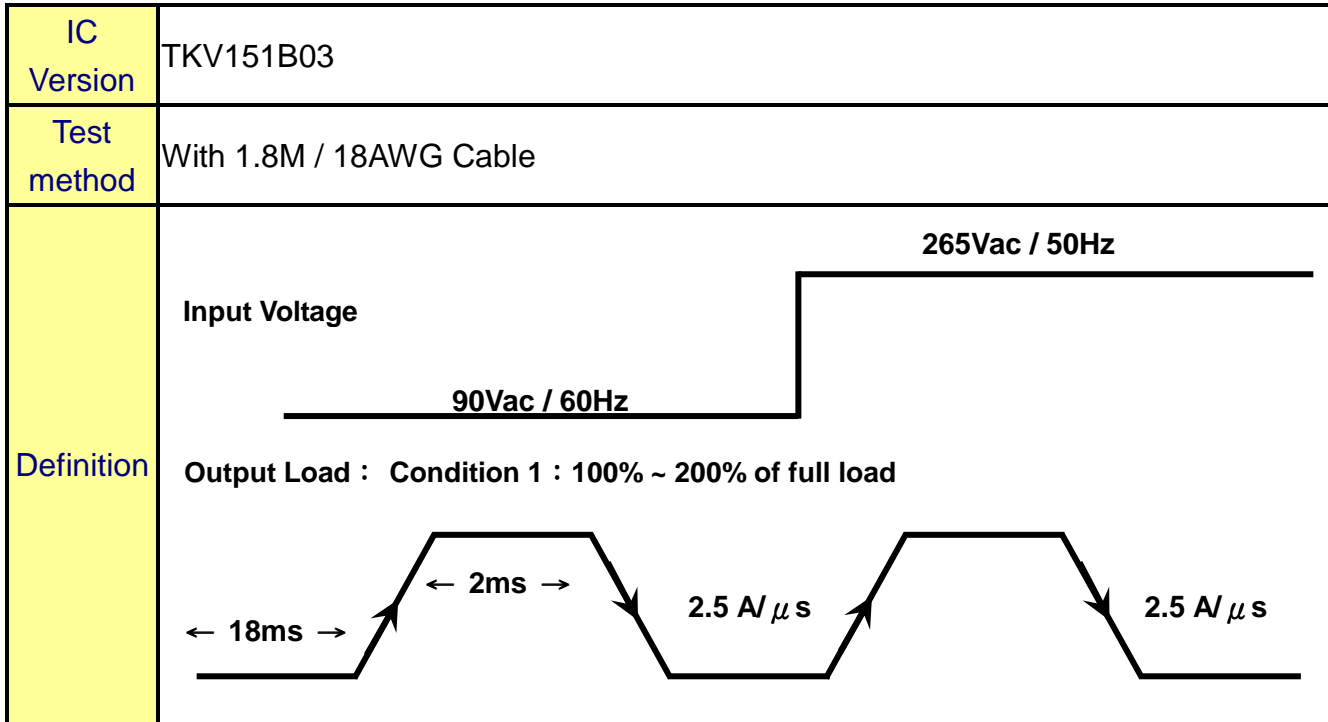


**Test Result**

Input Voltage	Measured Vo ( V )		Specification	Test result
	0% -- 100% load	100% -- 0% load		
90Vac / 60Hz	18.66	19.72	Vout ± 5 %	Pass
265Vac / 50Hz	18.96	19.69	Vout ± 5 %	Pass

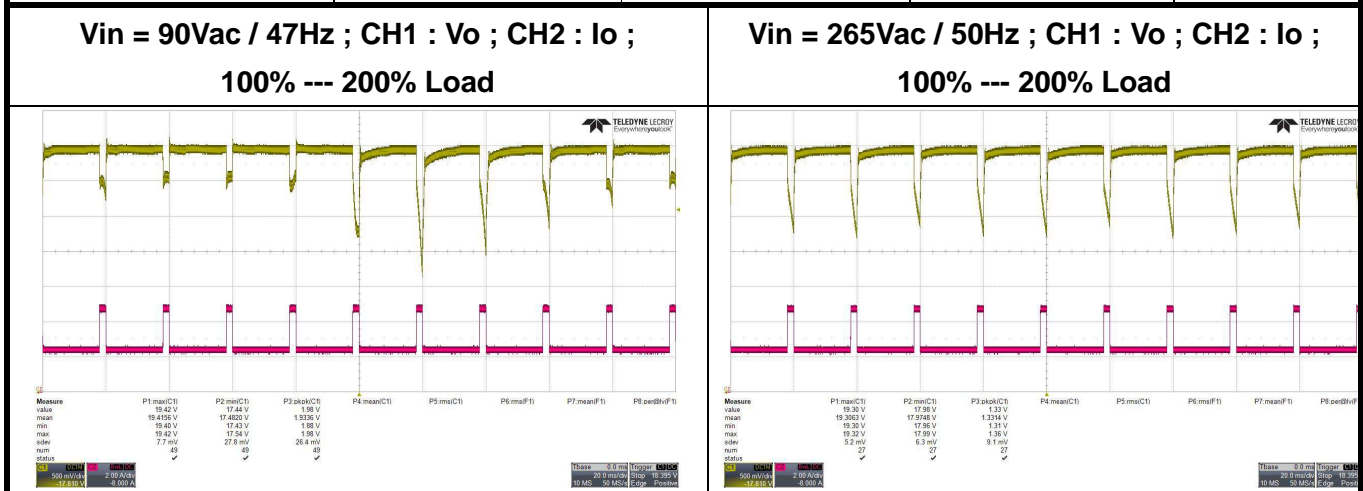


**5.2.6 Peak Load transient and cycle test**



**Test Result**

Input Voltage	Measured Vo ( V )		Specification	Test result
	100% -- 200% load	200% -- 100% load		
90Vac / 60Hz	17.43	19.40	Vo_min > 17V	Pass
265Vac / 50Hz	17.97	19.31	Vo_min > 17V	Pass



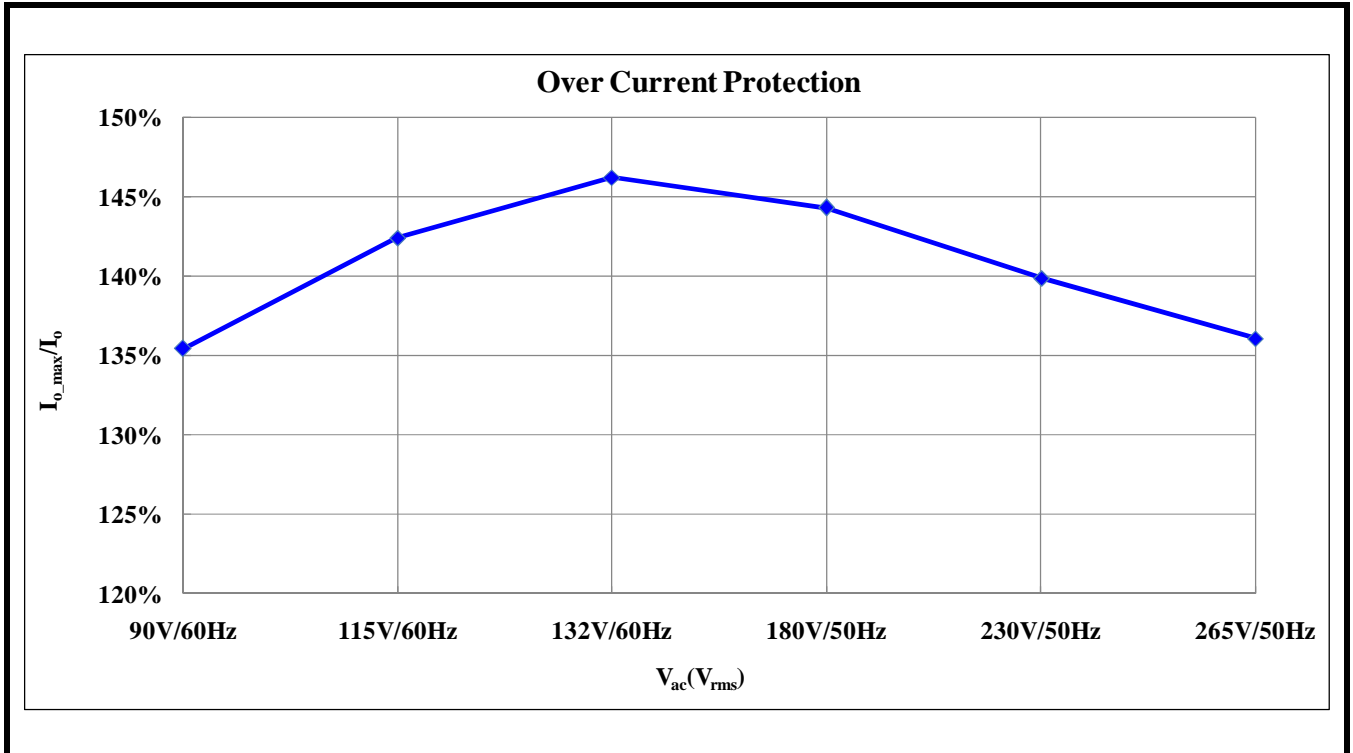
### 5.3 Protections

#### 5.3.1 OCP and recovery

IC Version	TKV151B03
Test method	
Test Condition	Rf=536Ω , Cf=470pF , Rcs=0.25Ω//20Ω

#### Test Result

Input Voltage	Output Current (A)	Io_max / Io (%)	Test result	Specification
90Vac / 60Hz	3.210	135.44	Pass	120% ~ 150% of the full load
115Vac / 60Hz	3.375	142.41	Pass	
132Vac / 60Hz	3.465	146.20	Pass	
180Vac / 50Hz	3.420	144.30	Pass	
230Vac / 50Hz	3.315	139.87	Pass	
265Vac / 50Hz	3.225	136.07	Pass	
△	0.255	10.76		

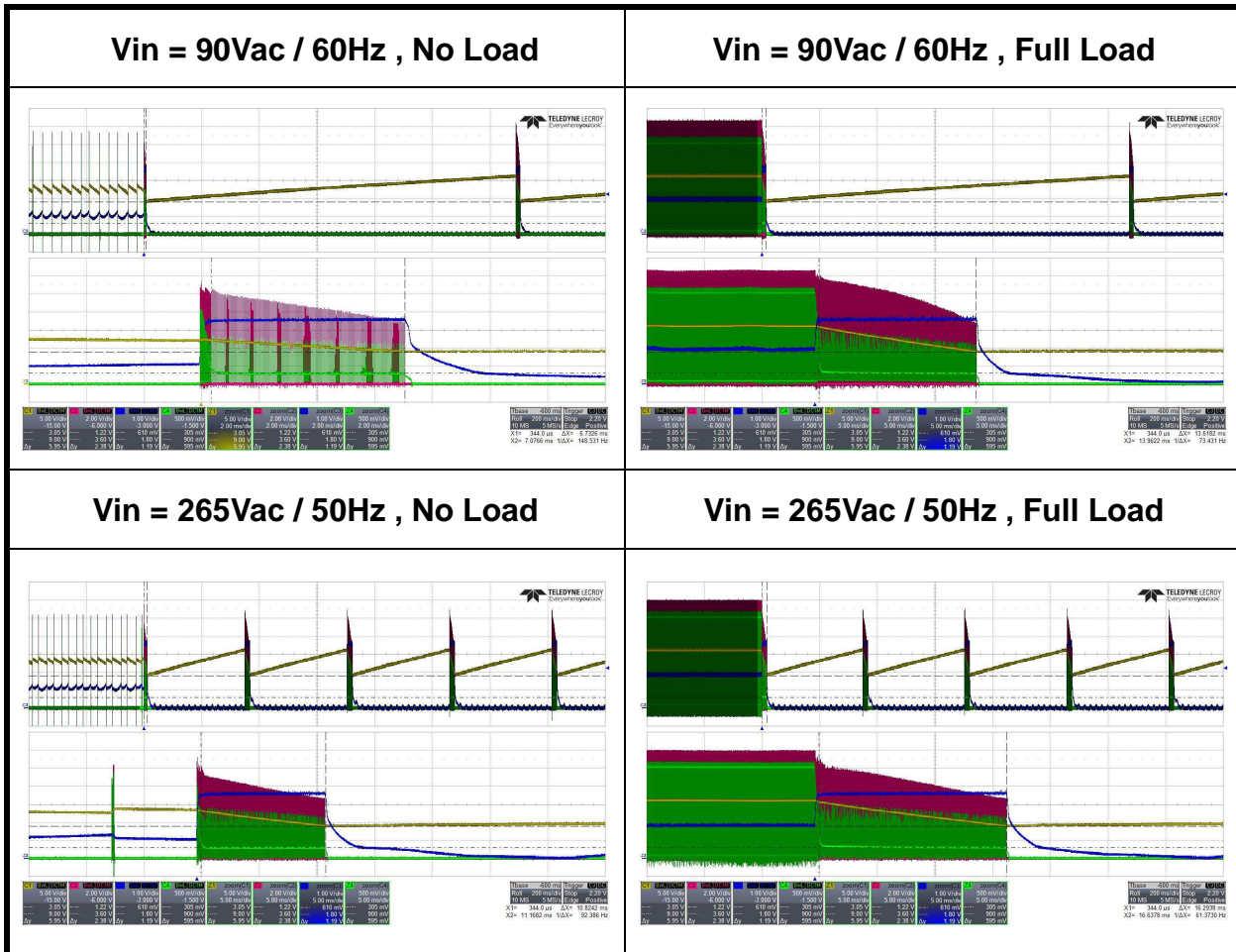




**5.3.2 SC and recovery**

IC Version	TKV151B03
Test method	
Test Condition	

Input Voltage	Output Load	Measured (mW)	Specification	Test Result
90Vac / 60Hz	0A	151.9 (UVLO)	Auto Recovery	Pass
	2.37A	156.5 (UVLO)		Pass
265Vac / 50Hz	0A	736.6 (UVLO)		Pass
	2.37A	749.0 (UVLO)		Pass

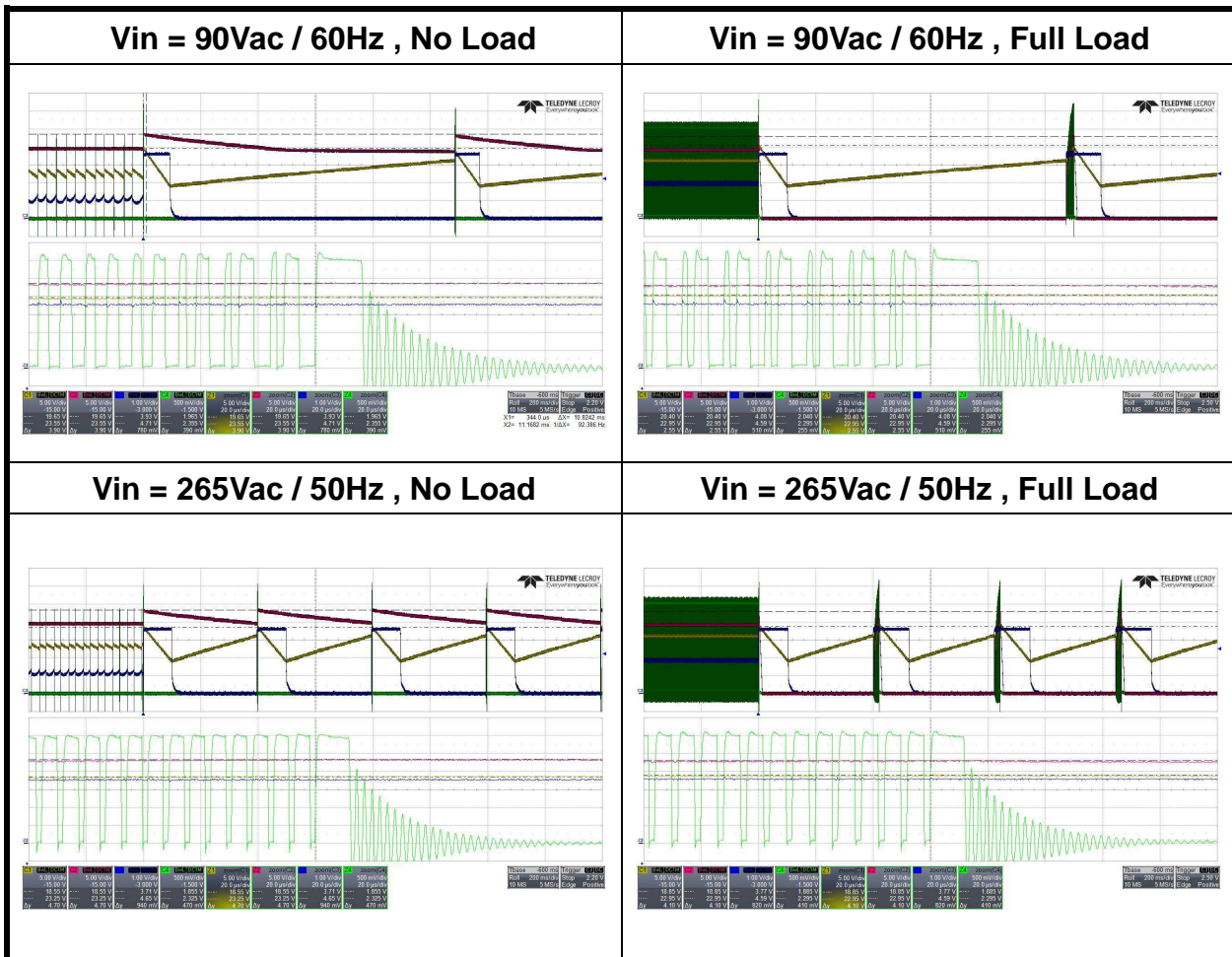


Note: CH1:V<sub>DD</sub>, CH2:V<sub>GATE</sub>, CH3:V<sub>COMP</sub>, CH4:V<sub>DMAG</sub> waveforms must be taken simultaneously.

**5.3.3 Photo-coupler short (Vout OVP)**

IC Version	TKV151B03
Test method	Measured with 18AWG/1.8M cable
Test Condition	

Input Voltage	Output Load	Output Voltage (V)	Measured (W)	Specification
90Vac / 60Hz	0A	23.55	0.113	Auto Recovery (Vout OVP)
	2.37A	22.95	1.725	
265Vac / 50Hz	0A	23.25	0.309	
	2.37A	22.95	3.118	

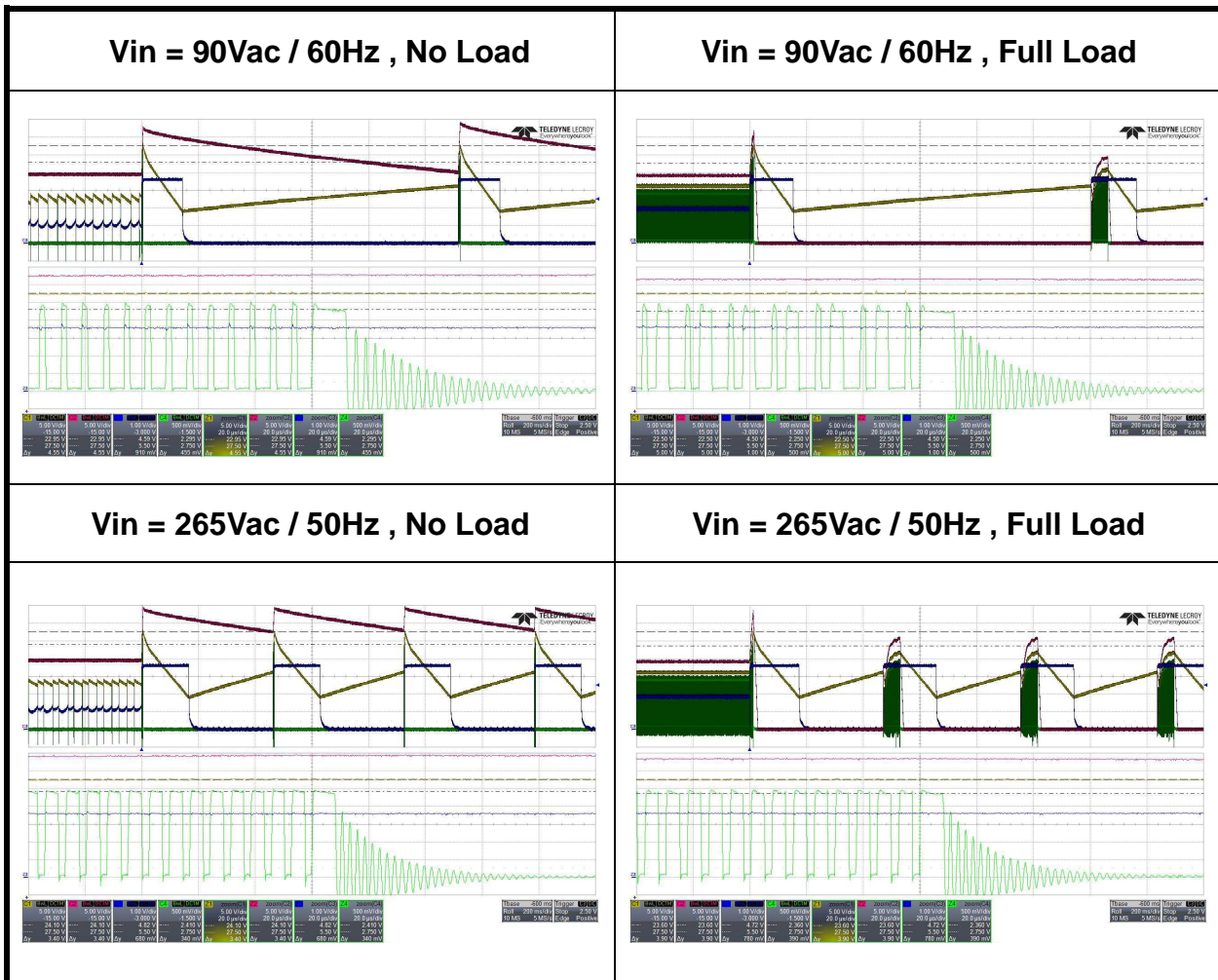


Note: CH1:V<sub>DD</sub>, CH2:V<sub>O</sub> CH3:V<sub>COMP</sub>, CH4:V<sub>DMAG</sub> waveforms must be taken simultaneously.

**5.3.4 Photo-coupler short (VDD OVP)**

IC Version	TKV151B03
Test method	Measured with 18AWG/1.8M cable
Test Condition	R <sub>21</sub> =10k

Input Voltage	Output Load	VDD Voltage (V)	Measured (W)	Specification
90Vac / 60Hz	0A	27.50	0.431	Auto Recovery (VDD OVP)
	2.37A	27.50	3.451	
265Vac / 50Hz	0A	27.50	0.597	
	2.37A	27.50	8.118	

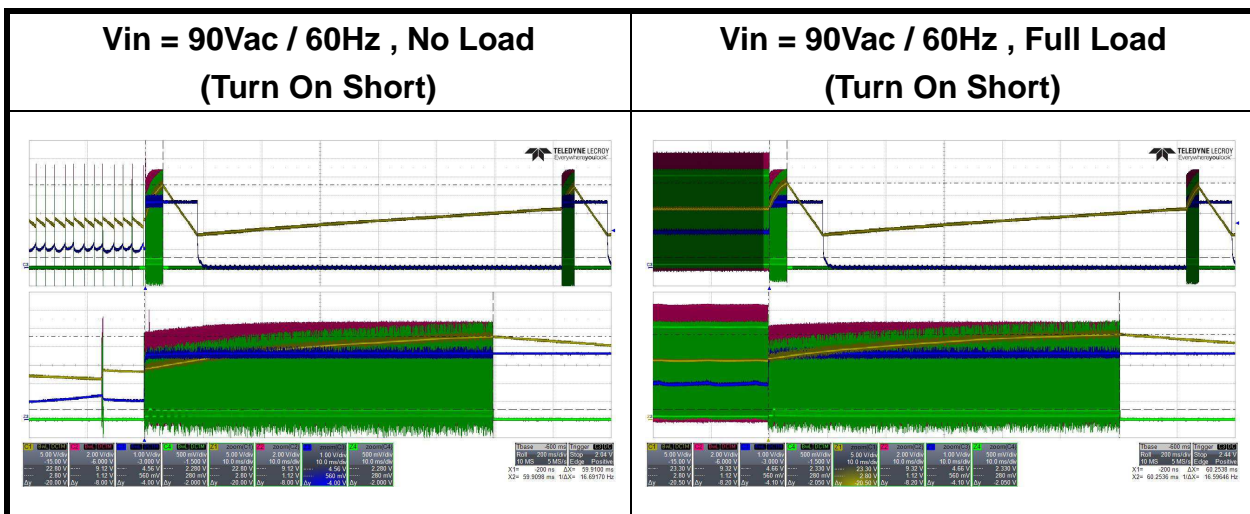


**Note: CH1:V<sub>DD</sub>, CH2:V<sub>O</sub>, CH3:V<sub>COMP</sub>, CH4:V<sub>DMAG</sub> waveforms must be taken simultaneously.**

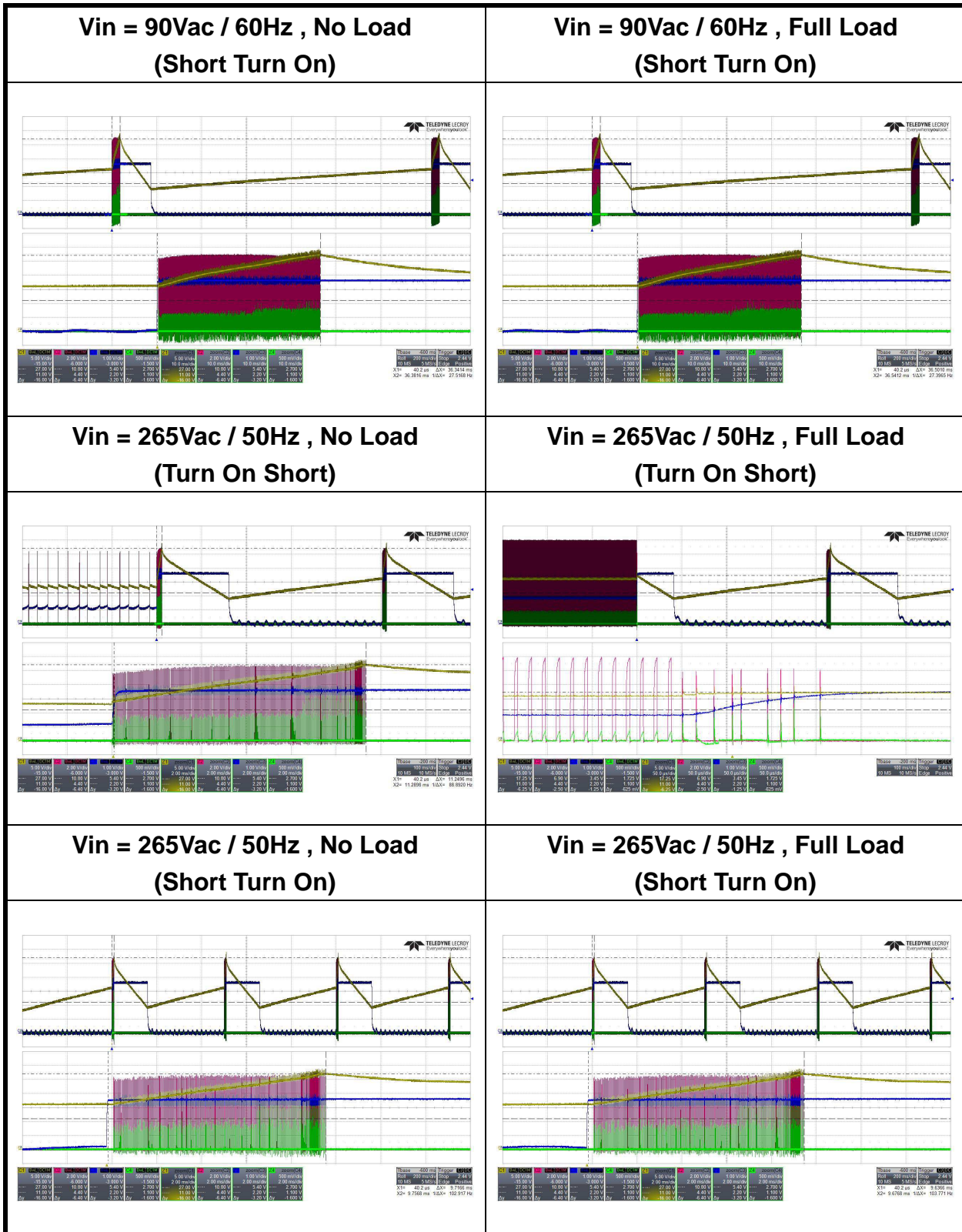
**5.3.5 Output diode short at 90Vac and 265Vac**

IC Version	TKV151B03
Test method	
Test condition	

Input Voltage	Output Load	Conduction	Specification
90Vac / 60Hz	0A Turn On Short	DMAG UVP	Auto Recovery
	Full Load Turn On Short	DMAG UVP	
	0A Short Turn On	VDD OVP	
	Full Load Short Turn On	VDD OVP	
265Vac / 50Hz	0A Turn On Short	VDD OVP	
	Full Load Turn On Short	SRSP (OC1.1)	
	0A Short Turn On	VDD OVP	
	Full Load Short Turn On	VDD OVP	





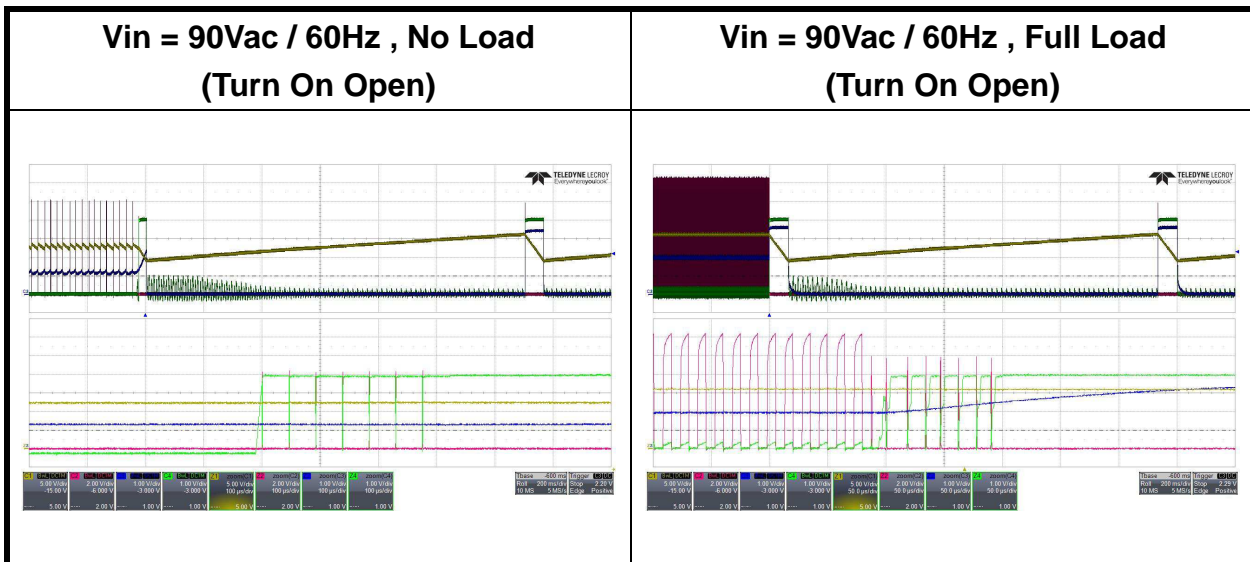


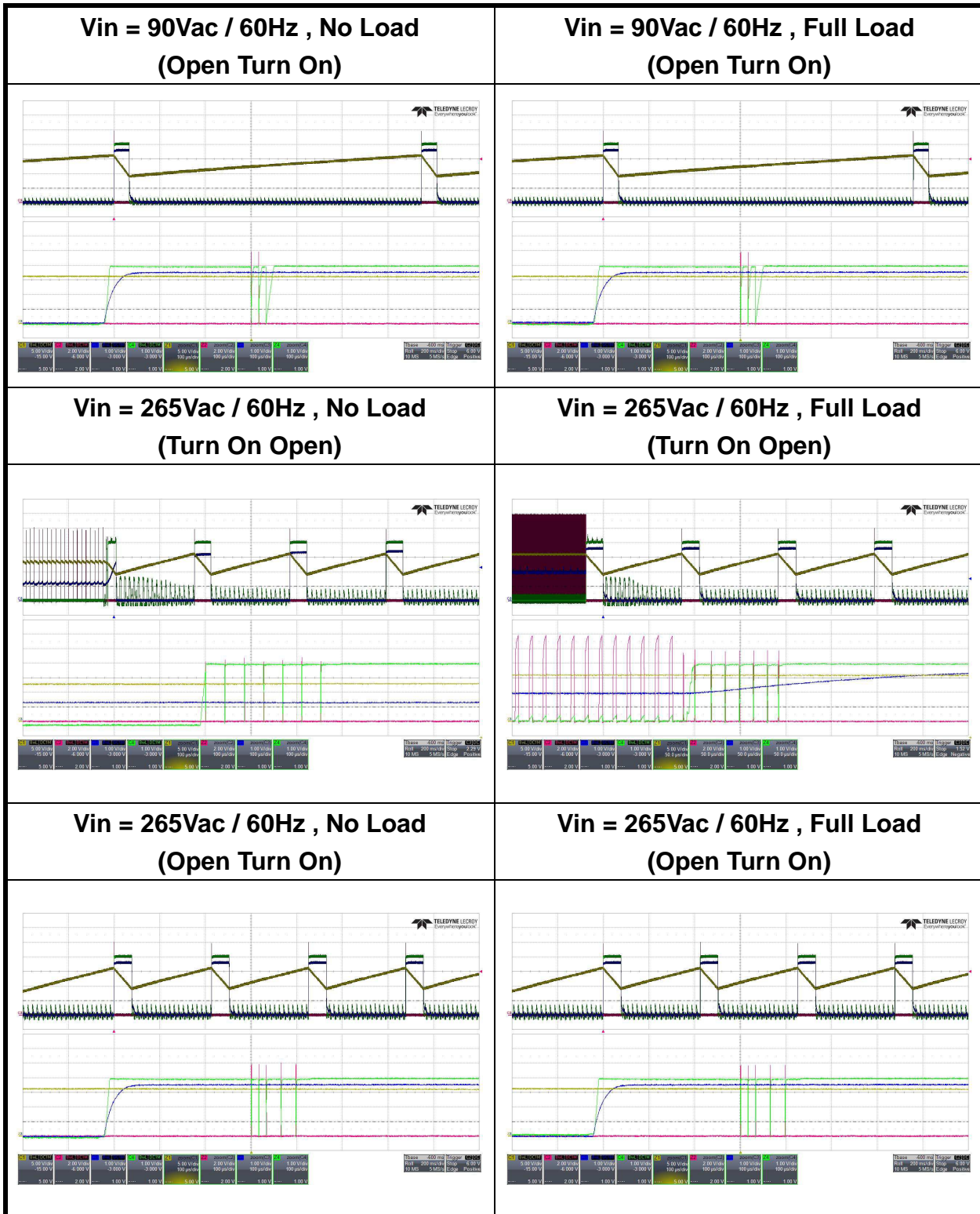
**Note: CH1:V<sub>DD</sub>, CH2:V<sub>GATE</sub>, CH3:V<sub>COMP</sub>, CH4:V<sub>CS</sub> waveforms must be taken simultaneously.**

**5.3.6 Rcs open at 90Vac and 265Vac**

IC Version	TKV151B03
Test method	

Input Voltage	Output Load	Conduction	Specification
90Vac / 60Hz	0A Turn On Open	Auto Recovery (OC1.1)	Auto Recovery
	Full Load Turn On Open	Auto Recovery (OC1.1)	
	0A Open Turn On	Auto Recovery (CSSP)	
	Full Load Open Turn On	Auto Recovery (CSSP)	
265Vac / 50Hz	0A Turn On Open	Auto Recovery (OC1.1)	
	Full Load Turn On Open	Auto Recovery (OC1.1)	
	0A Open Turn On	Auto Recovery (OC1.1)	
	Full Load Open Turn On	Auto Recovery (OC1.1)	



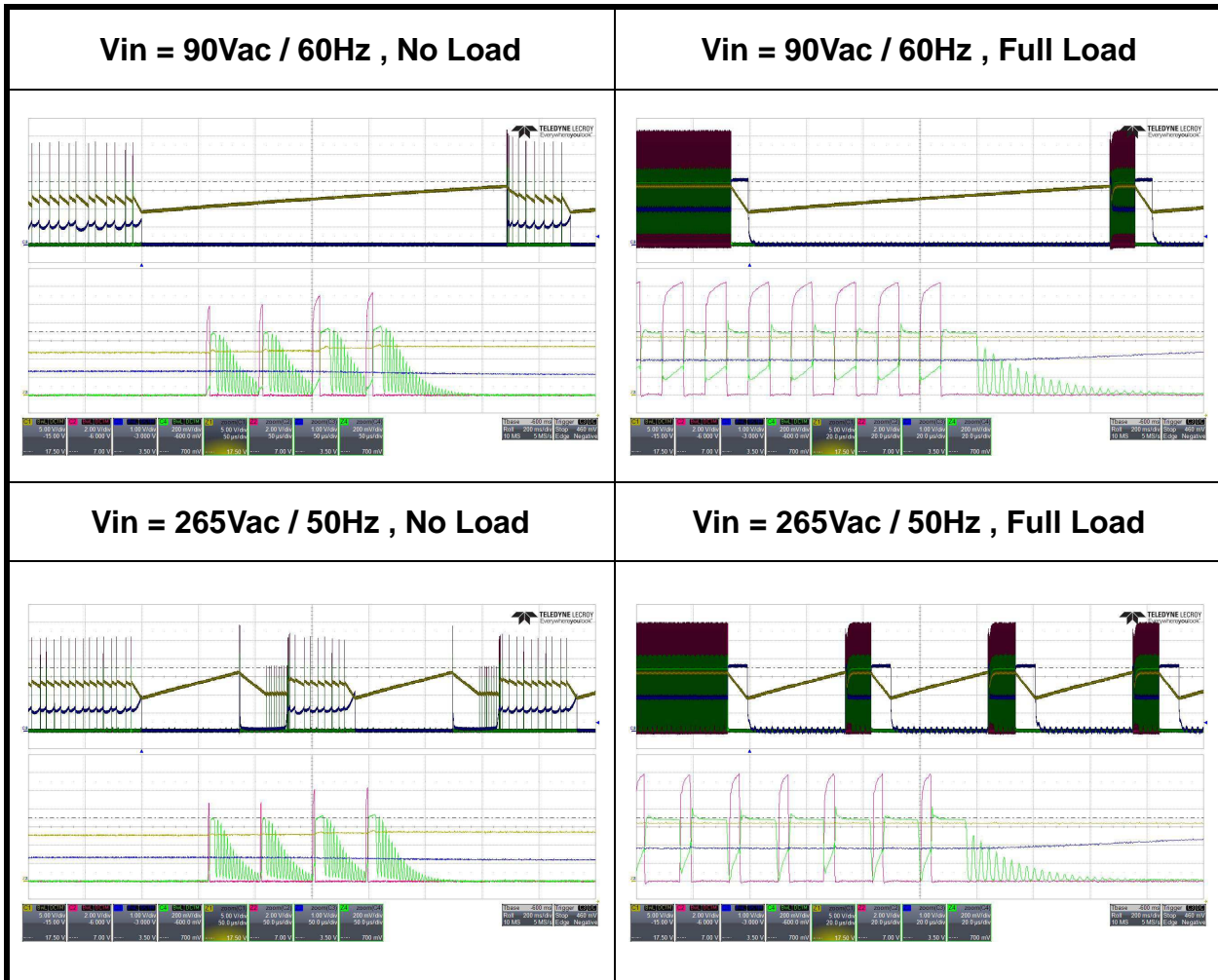


Note: CH1:V<sub>DD</sub>, CH2:V<sub>GATE</sub>, CH3:V<sub>COMP</sub>, CH4:V<sub>CS</sub> waveforms must be taken simultaneously.

**5.3.7 Over Temperature Protection**

IC Version	TKV151B03
Test method	
Test Condition	<b>1N4148+50kΩ VR</b>

Input Voltage	Output Load	R <sub>NTC</sub> (KΩ)	Specification	Test Result
90Vac / 60Hz	0A	9.223	Auto Recovery	Pass
	2.37A	11.303		Pass
265Vac / 50Hz	0A	9.651		Pass
	2.37A	11.352		Pass



Note: CH1:V<sub>DD</sub>, CH2:V<sub>GATE</sub>, CH3:V<sub>COMP</sub>, CH4:V<sub>CS</sub> waveforms must be taken simultaneously.



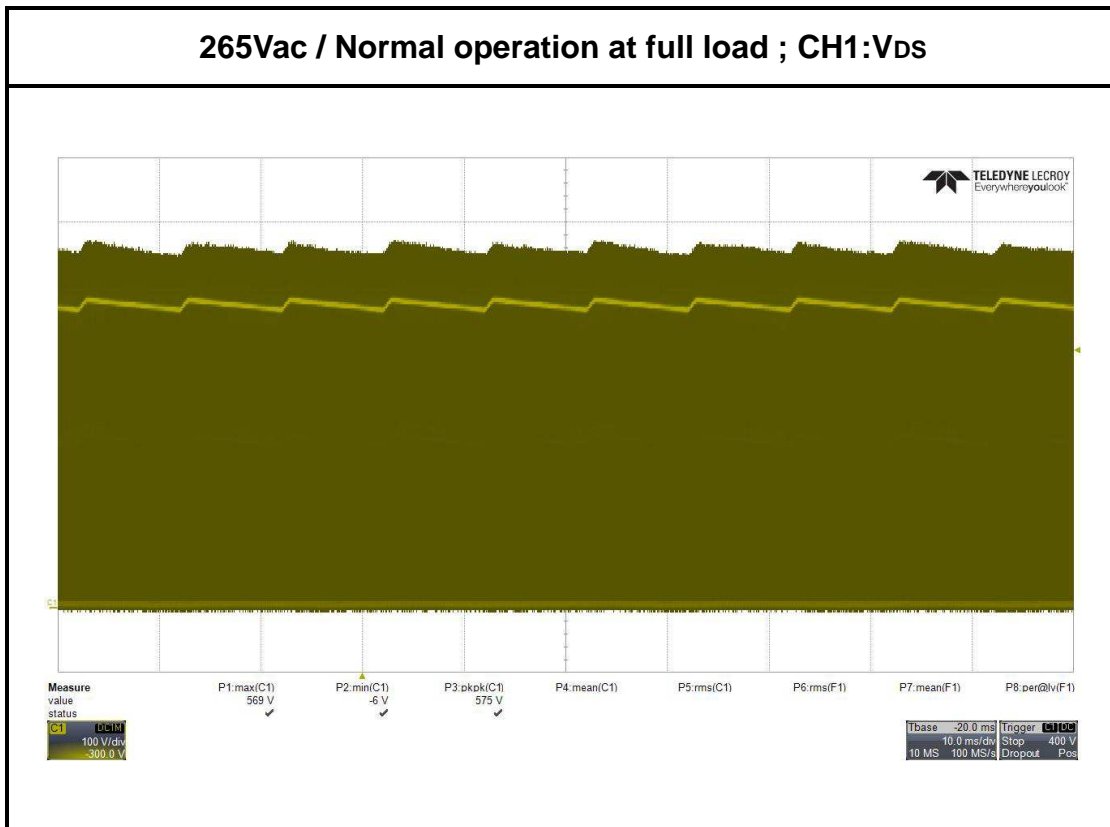
## 5.4 Key Component Waveforms

### 5.4.1 Voltage stress on MOSFET

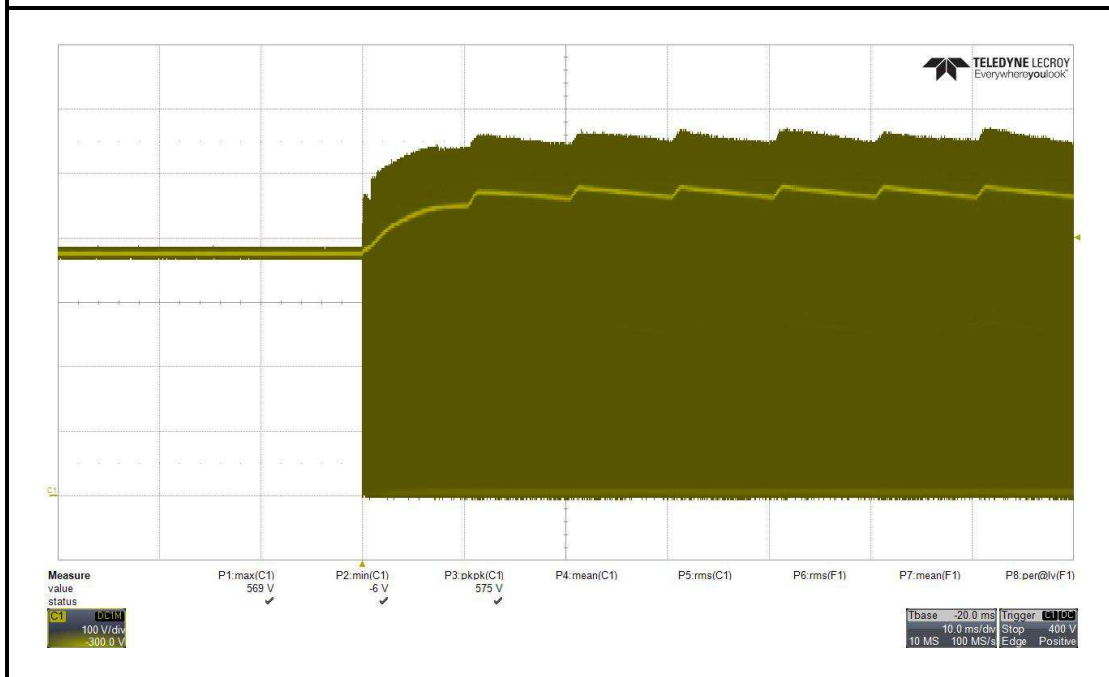
IC Version	TKV151B03
Test method	

#### Test Result

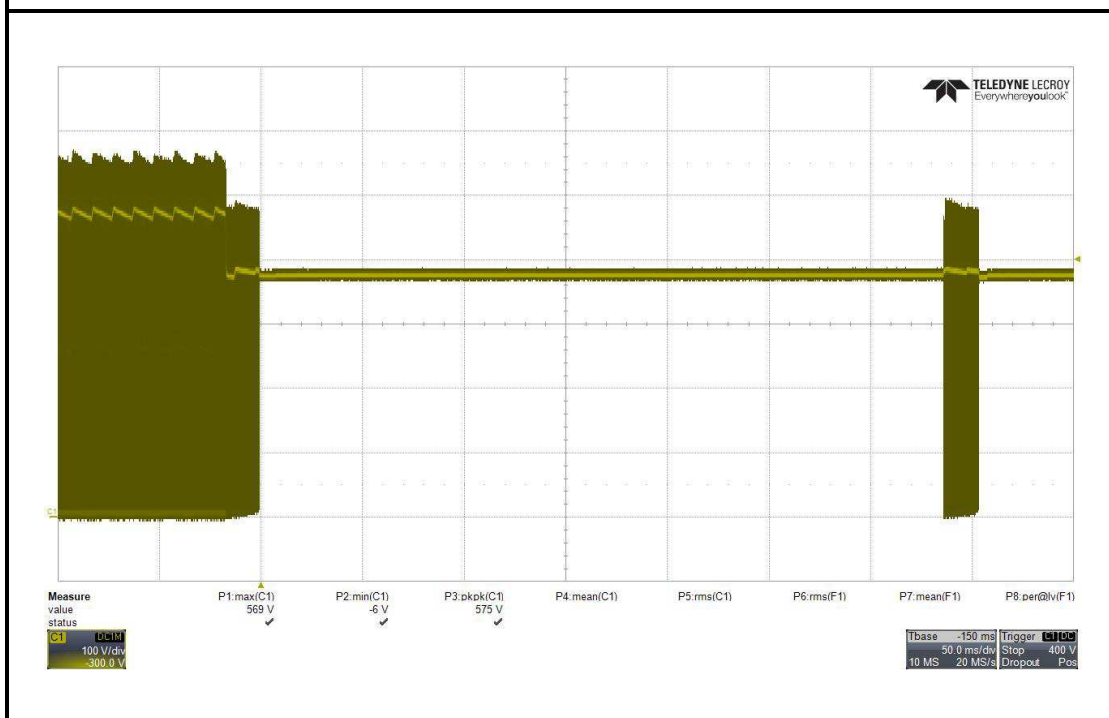
Input Voltage	Test Condition	Stress on MOSFET (V)	Component Rating
265Vac / 50Hz	Normal operation at full load	569	600V
	Start-up at full load	569	
	Output short at full load	569	



265Vac / Start-up at full load ; CH1:V<sub>DS</sub>



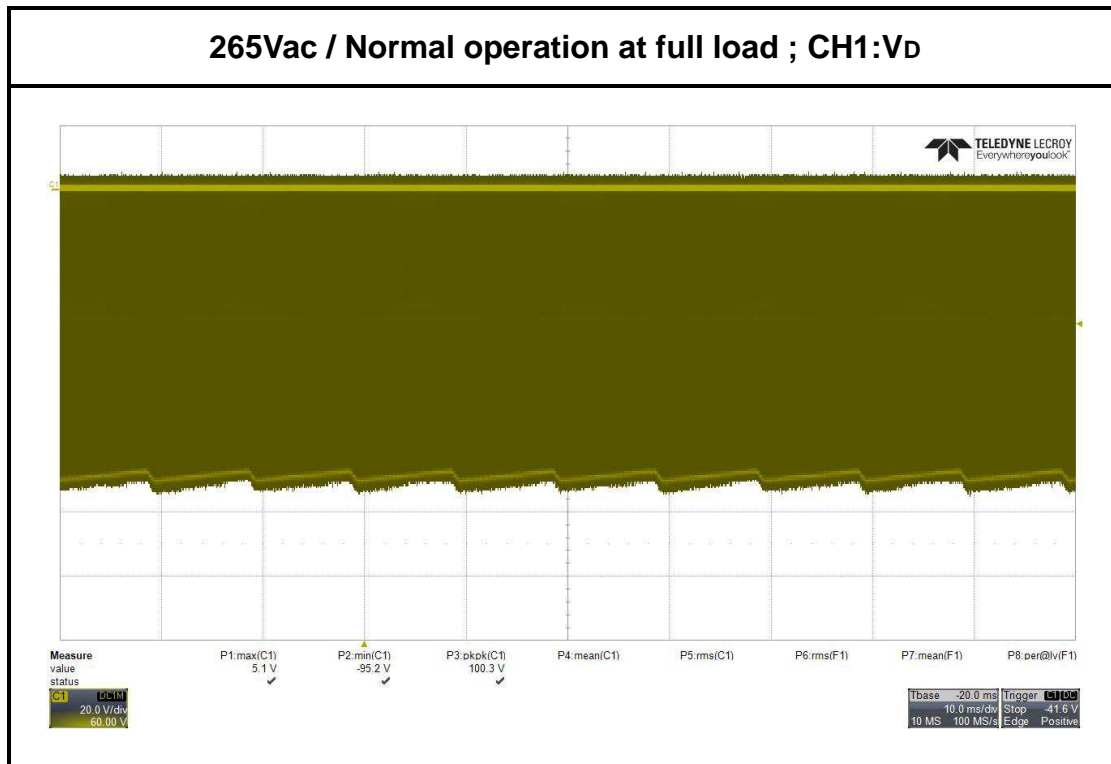
265Vac / Output short at full load ; CH1:V<sub>DS</sub>

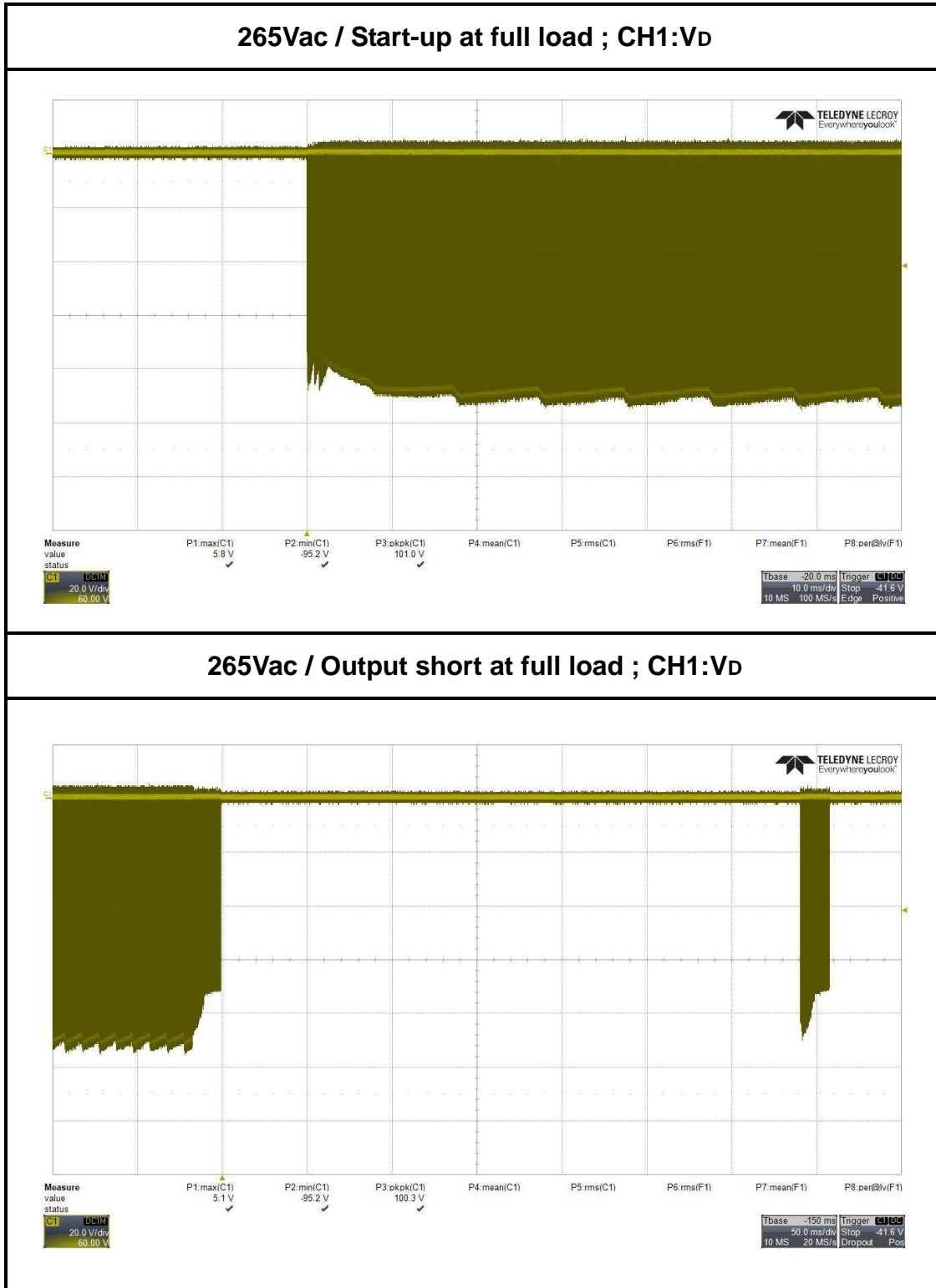


**5.4.2 Voltage stress on Secondary Rectifier Diode**

IC Version	TKV151B03
Test method	

Input Voltage	Test Condition	Stress on Diode (V)	Component Rating
265Vac/50Hz	Normal operation at full load	95.2	120V
	Start-up at full load	95.2	
	Output short at full load	95.2	

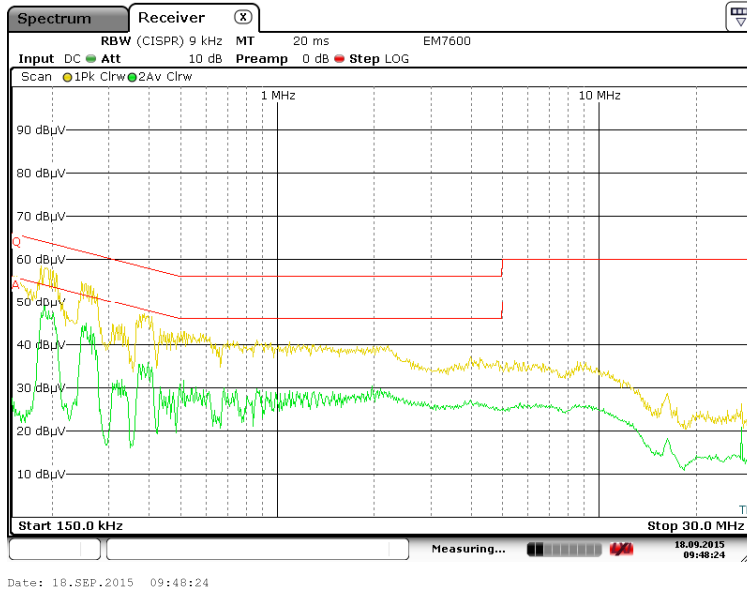




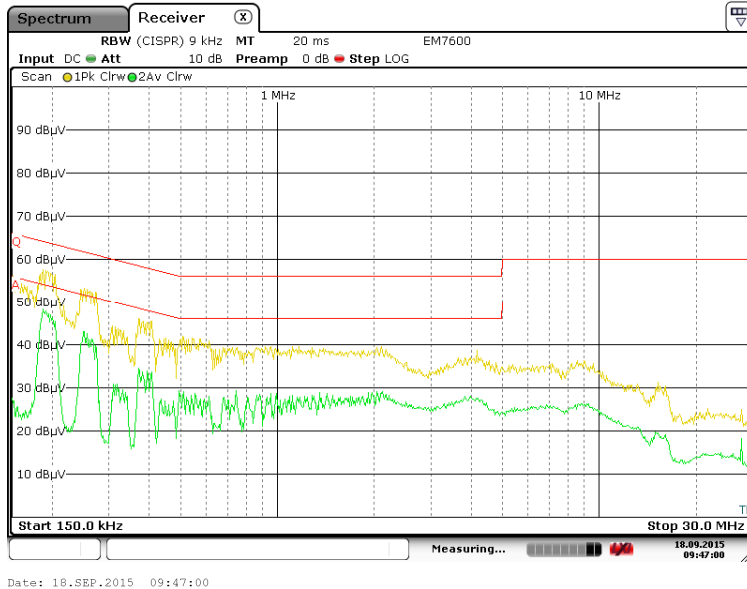
## 5.5 Standard and safety

### 5.5.1 EMI (Test result is peak & Output ground connector to safety ground)

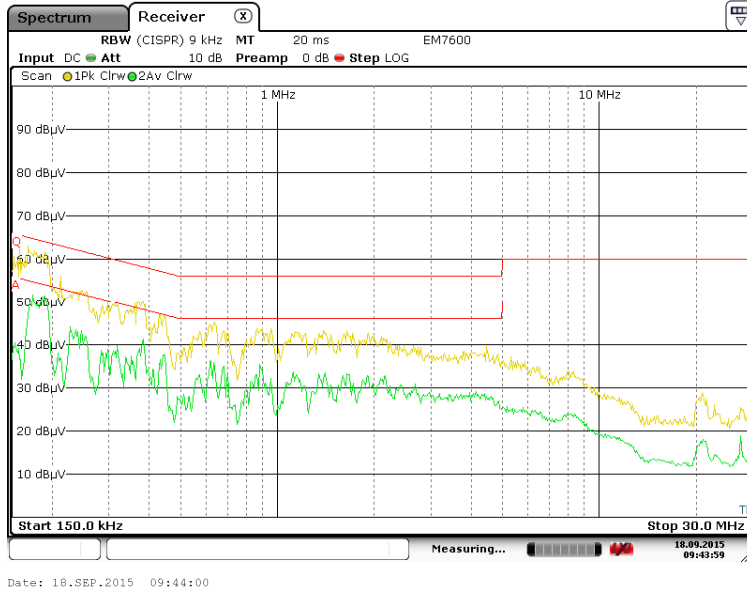
#### Conduction-Line @ Vin = 115Vac/60Hz ; Vo=Full load



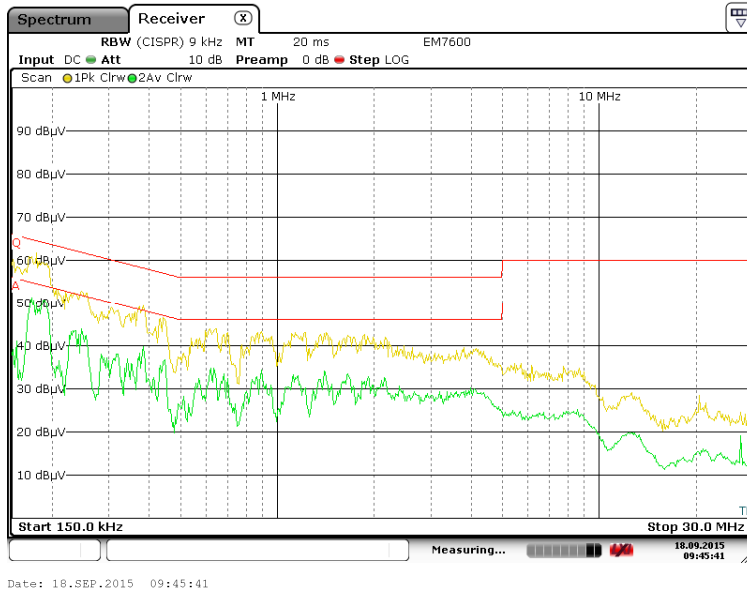
#### Conduction-Neutral @ Vin = 115Vac/60Hz ; Vo=Full load



**Conduction-Line @ Vin = 230Vac/50Hz ; Vo=Full load**



**Conduction-Neutral @ Vin = 230Vac/50Hz ; Vo=Full load**



## 6. Bill of Material

No.	Classification	Description	Ref.	Q'ty
1	Fuse	CQMST T5A/250V	F1	1
2	Varistor	14471	MOV1	1
3	Transformer	RM10 / 6-Pin	T1	1
4	L.F	Common choke / 5.5*11 / 150uH	LF1	1
5	L.F	Common choke / UU10.5 / 0.45*52Ts / 9mH min	LF2	1
6	Choke	Common choke / 7*11 / 380uH	L3	1
7	PCB	90mm * 40mm * 1.6mm	PCB	1
8	H's	For Q1	HS1	1
9	H's	For D2	HS2	1
10	X-CAP	0.22uF / 250V / X1 / 14mm*8mm*13mm (Pitch=13mm)	CX1	1
11	Electrolytic Capacitor	82uF / 400V / 16 $\phi$ *25mm / 105°C / L-tec TY serise	C1	1
12	CAP	1000pF / 1KV (Pitch=5mm)	C2	1
13	MLCC	Option / (0805)	C3	0
14	MLCC	X7R 1000pF / 50V (0805)	C4	1
15	MLCC	X7R 470pF / 50V (0805)	C5	1
16	MLCC	X7R 0.1uF / 50V (0805)	C6	1
17	Electrolytic Capacitor	4.7uF / 50V / 5 $\phi$ *11mm / 105°C / L-tec TH serise	C7	1
18	MLCC	X7R 1000pF / 1KV (1206)	C8	1
19	Electrolytic Capacitor	470uF / 25V / 10 $\phi$ *16mm / 105°C / L-tec LZG serise	C9	1
20	Electrolytic Capacitor	470uF / 25V / 10 $\phi$ *16mm / 105°C / L-tec LZG serise	C10	1

21	MLCC	Option / (1206)	C11	0
22	MLCC	X7R 0.1uF / 50V (1206)	C12	1
23	MLCC	X7R 47nF / 50V (0805)	C13	1
24	MLCC	Option / (0805)	C14	0
25	CAP	0.01uF / 500V / 8 $\phi$ (Pitch = 5mm)	C16	1
26	Y-CAP	1000pF / 250V 8 $\phi$ / X1Y1 (Pitch = 10mm)	CY1	1
27	RES-SMD	2.2M $\Omega$ ( $\pm 5\%$ ) / 1206	R1	1
28	RES-SMD	2.2M $\Omega$ ( $\pm 5\%$ ) / 1206	R2	1
29	RES-SMD	200K $\Omega$ ( $\pm 5\%$ ) / 1206	R3	1
30	RES-SMD	200K $\Omega$ ( $\pm 5\%$ ) / 1206	R3A	1
31	RES-SMD	0 $\Omega$ ( $\pm 5\%$ ) / 1206	R4	1
32	RES-SMD	Option / 0805	R7	0
33	RES-SMD	100 $\Omega$ ( $\pm 5\%$ ) / 0805	R8	1
34	RES-SMD	10 $\Omega$ ( $\pm 5\%$ ) / 0805	R8A	1
35	RES-SMD	536 $\Omega$ ( $\pm 1\%$ ) / 1206	R9	1
36	RES-MOF	0.25 $\Omega$ ( $\pm 1\%$ ) / 1W	R10	1
37	RES-SMD	20 $\Omega$ ( $\pm 1\%$ ) / 1206	R10A	1
38	RES-SMD	2.2 $\Omega$ ( $\pm 5\%$ ) / 1206	R11	1
39	RES-SMD	47 $\Omega$ ( $\pm 5\%$ ) / 1206	R12	1
40	RES-SMD	47 $\Omega$ ( $\pm 5\%$ ) / 1206	R12A	1
41	RES-SMD	2K $\Omega$ ( $\pm 5\%$ ) / 0805	R13	1
42	RES-SMD	5.1K $\Omega$ ( $\pm 5\%$ ) / 0805	R14	1



43	RES-SMD	27K $\Omega$ ( $\pm 5\%$ ) / 0805	R15	1
44	RES-SMD	68K $\Omega$ ( $\pm 1\%$ ) / 0805	R16	1
45	RES-SMD	10K $\Omega$ ( $\pm 1\%$ ) / 0805	R17	1
46	RES-SMD	750K $\Omega$ ( $\pm 1\%$ ) / 0805	R17A	1
47	RES-SMD	105K $\Omega$ ( $\pm 1\%$ ) / 0805	R20	1
48	RES-SMD	20K $\Omega$ ( $\pm 1\%$ ) / 0805	R21	1
49	RES-SMD	0 $\Omega$ ( $\pm 5\%$ ) / 0805	R24	1
50	PWM	RT7739GGE / SOT-23-6	U1	1
51	Photo coupler	PC817 (C-Type) / DIP-4	U2	1
52	Shunt regulator	TL431 / SOT-23	U3	1
53	N-MOSFET	NDF10N60ZG / 10A--600V / TO220FP / Rds(on)=0.65 $\Omega$	Q1	1
54	Bridge Diode	FL406G / 4A--600V	BD1	1
55	Fast Diode	FR107 / 1000V--1A / DO-41 / VF=1.2V	D1	1
56	General Diode	1N4003 / 200V--1A / DO-41 / VF=1.0V	D2	1
57	Schottky Diode	V40120C / 120V—40A / TO-220 / VF=0.43V	D3	1
58	Fast Diode	1N4148 / 75V—200mA / SOD-323	D4	1
59	Fast Diode	Option / SOD-123	D5	0
60	LED	4 $\phi$ LED	LED	1
61	NTC	SCK102R55 / 10 $\phi$	NTC1	1
62	NTC	Option / 5 $\phi$	NTC2	0
63	Jump	23mm / 0.6 $\phi$	JP1	1
64	Jump	12mm / 0.6 $\phi$	JP2	1

65	Jump	15mm / 0.6 $\phi$	JP3	1
66	Jump	8mm / 0.6 $\phi$	JP4	1



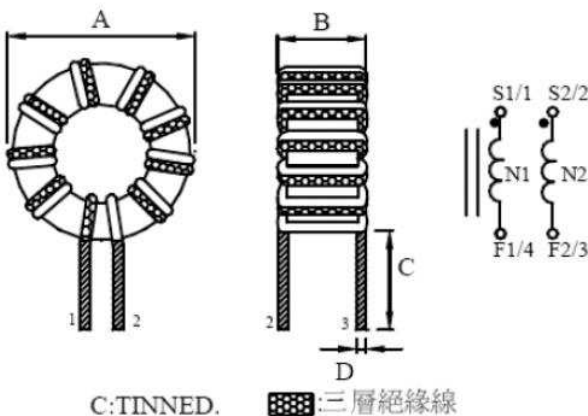
**WINDING TABLE: (繞線結構)**

Winding No. (組別)	PIN (腳位)	Wire & Wire Copper (線徑 x 股數 x 層數)	Turns (圈數)	Winding Tape (繞線方式)	Tape Layer (膠帶層次)
<i>Bobbin</i>					
N1	6 → 4	0.35 x 2P x 2L	26Ts	密繞	1L
E1	1.2T 銅箔(開迴路)焊一條線於 PIN 2			背膠	3L
N2	A → B	Triple wire 0.45x3Px2L	10Ts	密繞	3L
E2	1.2T 銅箔(開迴路)焊一條線於 PIN 2			背膠	1L
N3	4 → 5	0.35 x 2P x 2L	26Ts	密繞	3L
N4	3 → 2	0.2 x 1P	8Ts	靠下邊密繞	1L
<i>Core – RM10</i>				950uH	3L

Note1: Cut PIN1、PIN4

Note2: Varnished

**LF1 Specification**

耕興股份有限公司 SPORTON INTERNATIONAL INC. SPEC. FOR APPROVAL			
OUR PART NO. : <b>TCN0903-191</b>		SIZE SPEC. (UNIT m/m)	
 <p>C: TINNED. 三層絕緣線</p>		A	<b>11.5(Max)</b>
		B	<b>5.5(Max)</b>
		C	<b>10.0±1.0</b>
		D	<b>0.4±0.05</b>
ITEM	FREQUENCY	SPECIFICATION	REMARK
L(N1=N2)	100KHz/1V	190uH(Ref.)	<b>TEST INSTRUMENT:</b> ■ L: WK3260B ■ RDC: HP34401A  ※ No Varnish. ※ Weight: 1.00g
RDC(N1=N2)		0.034Ω(Max)	
CURRENT (N1=N2)		2.0A(Max)	
DRAWING BY	CHECKED BY	APPROVED BY	SAMPLE NO.
<b>IVY</b>	<b>FENNY</b>	<b>MILLER</b>	

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